

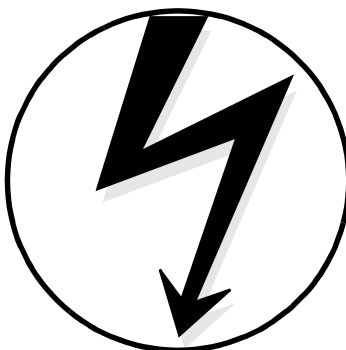


Z8932102ZEM

USER'S MANUAL

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Electrical



Safeguards

WARNING!

Follow the precautions listed below to avoid permanent damage to the emulator.

I. Always use a grounding strap to prevent damage resulting from electrostatic discharge (ESD).

II. Power-Up Precautions.

1. If the target application board has its own power, remove the U_V_{CC} jumper, J12.
2. Ensure that all power to the emulator and the target application (if any) is turned OFF.
3. Connect the target pod to the target application (if any).
4. Power up the emulator, then press the RESET button.
5. Power up the target application (if any).

III. Power-Down Precautions.

When powering down, follow this procedure in the precise order shown below:

1. Power down the target application board (if any).
2. Remove the target pod.
3. Power down the emulator.

NOTES:

1. Refer to the “Precaution List” section of the *Product Information* sheet for additional operating precautions specific to various devices.
 2. Do not leave the emulator powered up with the RS-232C cable connected to a powered-down PC.
 3. Before inserting target pod into target application board, refer to Chapter 2 to determine appropriate jumper selections and options.
-

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PREFACE

ABOUT THIS MANUAL

We recommend that you read and understand everything in this manual before setting up and using the product. However, we recognize that users have different styles of learning. Therefore, we have designed this manual to be used either as a how-to procedural manual or a reference guide to important data.

The following conventions have been adopted to provide clarity and ease of use:

- Courier Font For Executables

Commands, variables, icon names, entry field names, selection buttons, code examples, and other executable items are distinguished by the use of the Courier font. Where the use of the font is not possible, like in the Index, the name of the entity is capitalized. For example, a procedure may contain an instruction which appears as: Click on `File`. However, an Index entry would appear as `FILE`.

- Grouping of Actions Within A Procedure Step

Actions in a procedure step are all performed on the same window or dialog box. Actions performed on different windows or dialog boxes appear in separate steps.

- Sequencing Words Within A Procedure Step

When an item in a procedure contains a series of actions, the second action is preceded by the word *then*, and the third and subsequent actions are preceded by the word *and*. For example: Click on `View`, then `Memory`, and `Z8 Code Memory`.

- Unavailable menu items are presented in gray.

ADDITIONAL SOURCES OF INFORMATION

In addition to this manual, you should have access to and be familiar with the following documentation:

- Z89321/371 Preliminary Product Specification
- The new emulator Graphical User Interface (GUI) software features many enhancements, including an improved context-sensitive on-line help facility that provides brief messages on keyboard, emulator commands, and various procedures on how to use the emulator. Refer to the README . TXT file on the DSP GUI diskette for detailed information.

TRADEMARKS

Windows is a registered trademark of the Microsoft Corporation.



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TABLE OF CONTENTS

Chapter Title and Subsections	Page
Chapter 1. Introduction	
Overview	1-1
Supported Devices	1-1
GUI-Supported Compiler, Assembler Formats	1-2
Hardware Specifications	1-2
Operating Conditions	1-2
Dimensions	1-2
Serial Interface	1-2
Emulation Memory	1-2
Number of Breakpoints	1-2
Host Computer	1-3
Minimum Requirements	1-3
Recommended	1-3
Kit Contents	1-4
Chapter 2. Set-Up and Installation	
Software Installation	2-1
ZMASM/ZDS Installation	2-1
DSP ICEBOX GUI Installation	2-2
Program Uninstallation	2-2
Hardware Installation	2-3
Installation Procedure	2-3
Power Up	2-7
Power Down	2-7
Emulator Operation	2-7
Resetting	2-7

Chapter Title and Subsections	Page
Chapter 3. Summary of Menus, Commands, and Operations	
Main Menu	3-1
File Menu	3-2
Open Session	3-3
Save Session	3-3
Download Application	3-4
Download DSP RAM0	3-5
Download DSP RAM1	3-6
Download DSP Code Memory	3-7
Download DSP External Data Memory	3-8
Load Symbol	3-8
Upload DSP RAM0	3-9
Upload DSP RAM1	3-10
Upload DSP Code Memory	3-11
Upload DSP External Data Memory	3-12
Exit	3-12
View Menu	3-13
Registers	3-13
RAM Bank0	3-14
RAM Bank1	3-20
DSP Code Memory	3-23
Debug	3-25
OTP	3-33
OTP ROM	3-34
Programming Options	3-34
Command Status	3-35
Device Serialization	3-35
Configuration	3-36
Help	3-37
Window Refreshing	3-38
Emulator Operation	3-38
Hardware Reset	3-38
ROM Size	3-39
Breakpoint Implementation	3-39
EPROM/OTP Programmable Devices	3-39
Running Multiple Emulators	3-40
Evaluation Functions	3-41
Peripheral Interfaces	3-42
Initialization of Peripherals	3-42
External Data Memory	3-42

Chapter Title and Subsection	Page
Chapter 4. Sample Session	
DSP Emulator Sample Session	4-1
Starting the Application	4-2
DSP Microcontroller Emulation	4-3
Monitoring/Modifying Registers	4-7
Tracing Code Using DSP Debug	4-16
Window Refresh	4-25
Save and Quit	4-25
Chapter 5. Troubleshooting Guide	
Introduction	5-1
Initial Screen Does Not Appear	5-1
XTAL Oscillator Contention.	5-2
“Can’t Open Windows” Message	5-2
“Out of Synchronization with the Emulator” Message	5-2
Appendix A. Accessing the ZBBS/Internet	
Bulletin Board Information	A-1
How to Access the ZBBS	A-1
ZiLOG On The Internet	A-1
Appendix B. Emulator Schematic	
Emulator Schematic	B-1
Appendix C. ASCII Character Set	
ASCII Character Set	C-1
Appendix D. Problem/Suggestion Report Form	
Problem/Suggestion Report Form	D-1

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Totally Logical

LIST OF FIGURES

Figure Title	Page
Chapter 2. Set-Up and Installation	
Figure 2-1. Hook-Up Diagram	2-4
Figure 2-2. Option Jumper Locations	2-5
Chapter 3. Summary of Menus, Commands, and Operations	
Figure 3-1. Open Session Dialog Box	3-3
Figure 3-2. ZiLOG ICEBOX (Save Session) Dialog Box	3-3
Figure 3-3. Download Application Dialog Box	3-4
Figure 3-4. Download To DSP RAM0 Dialog Box	3-5
Figure 3-5. Download To DSP RAM1 Dialog Box	3-6
Figure 3-6. Download To DSP Code Memory Dialog Box	3-7
Figure 3-7. Download To External Data Memory Dialog Box	3-8
Figure 3-8. Save DSP RAM0 Dialog Box	3-9
Figure 3-9. Save DSP RAM1 Dialog Box	3-10
Figure 3-10. Save DSP Memory Dialog Box	3-11
Figure 3-11. Save DSP External Memory Dialog Box	3-12
Figure 3-12. Z89321 Registers Dialog Box	3-13
Figure 3-13. Z89321 RAM Bank0 Window	3-15
Figure 3-14. Memory Fill Dialog Box	3-16
Figure 3-15. Clear All Memory Confirmation Window	3-17
Figure 3-16. Address Dialog Box - Start Address	3-17
Figure 3-17. Address Dialog Box - Absolute Address	3-18
Figure 3-18. Tracking Pointer Register P0:0	3-18
Figure 3-19. Tracking Pointer Register P1:0	3-19
Figure 3-20. Tracking Pointer Register P2:0	3-19
Figure 3-21. Z89321 RAM Bank1 Window	3-20
Figure 3-22. Tracking Pointer Register P0:1	3-21
Figure 3-23. Tracking Pointer Register P1:1	3-21
Figure 3-24. Tracking Pointer Register P2:1	3-22

Figure Title	Page
---------------------	-------------

Chapter 3. Summary of Menus, Commands, and Operations (Continued)

Figure 3-25. DSP Memory Window	3-23
Figure 3-26. At Register Dialog Box	3-24
Figure 3-27. Program Counter Window	3-24
Figure 3-28. Z89321 Debug Dialog Box	3-25
Figure 3-29. Z89321 Debug Fields	3-26
Figure 3-30. Debug Log Window	3-30
Figure 3-31. DSP Source Window	3-31
Figure 3-32. Source Module Window	3-32
Figure 3-33. Output Window	3-32
Figure 3-34. OTP Window	3-33
Figure 3-35. Configuration Dialog Box	3-37
Figure 3-36. Multiple Emulator Operation Using 2 COM Ports	3-40

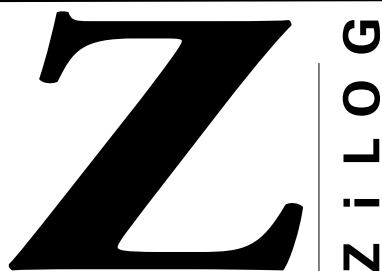
Chapter 4. Sample Session

Figure 4-1. ZiLOG ICEBOX Connect Dialog Box	4-2
Figure 4-2. Configuration Dialog Box	4-3
Figure 4-3. Configuration Dialog Box With 64K ROM Size Selected	4-4
Figure 4-4. Download Application Dialog Box	4-5
Figure 4-5. Load File Browse Dialog Box	4-5
Figure 4-6. Load File Browse Dialog Box With SUITE.LD File Selection	4-6
Figure 4-7. Download Application Dialog Box With SUITE.LD Selection	4-6
Figure 4-8. Z89321 Registers Dialog Box	4-7
Figure 4-9. Z89321 RAM Bank0 Window	4-8
Figure 4-10. Memory Fill Dialog Box	4-9
Figure 4-11. Z89321 RAM Bank0 Dialog Box With Memory Block 0020 - 00FF Zeroed	4-9
Figure 4-12. Clear All Memory Window	4-10
Figure 4-13. Z89321 RAM Bank0 Dialog Box With Zeroed Registers	4-10
Figure 4-14. Z89321 Registers and Z89321 RAM Bank1 Windows	4-11
Figure 4-15. RAM Bank1 With Value 00ADH At Location 09H	4-12
Figure 4-16. Set Pointer P0:1	4-12
Figure 4-17. Tracking Pointer P0:1 in RAM Bank1	4-13
Figure 4-18. DSP Code Memory, Z89321 Registers, and Z89321 RAM Bank1 Windows	4-14
Figure 4-19. The Z89321 Registers, RAM Bank1, and Z89321 DSP Memory Windows	4-15
Figure 4-20. Z89321 Debug Dialog Box	4-16
Figure 4-21. Debug Log Dialog Box	4-17
Figure 4-22. Z89321 Debug Window With Jump Input	4-18
Figure 4-23. Z89321 Debug Window After Jump	4-18
Figure 4-24. Z89321 Debug Window After RESET	4-19
Figure 4-25. Z89321 Debug Window With First Breakpoint Set	4-19
Figure 4-26. Z89321 Debug Window At Breakpoint After GO	4-20
Figure 4-27. Z89321 Debug Window With Two Breakpoints	4-21

Chapter 4. Sample Session (Continued)

Figure 4-28. Z889321 Debug Window After GO	4-21
Figure 4-29. Z889321 Debug Window With Second Breakpoint Cleared	4-22
Figure 4-30. Z89321 Debug Window With Original Breakpoint Cleared	4-22
Figure 4-31. Z89321 Debug Window After HALT	4-23
Figure 4-32. DSP Log File Sample	4-24
Figure 4-33. Save Session Dialog Box	4-25
Figure 4-34. Open Session Dialog Box	4-25

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CHAPTER 1

INTRODUCTION

OVERVIEW

Congratulations for selecting a fine development tool! The ZiLOG Z8932101ZEM Emulator is carefully engineered to provide the best balance between low-cost and useful features to shorten your development time for Z89321-based products. The DSP GUI Emulator graphical user interface (GUI) software runs under MS-Windows[®], supporting selected ZiLOG devices. Also included is the ZiLOG Macro Cross Assembler (ZMASM), a full-featured assembler that runs under Windows.

SUPPORTED DEVICES

Table 1-1. Supported Devices

Packages	Emulation	OTP Programming
40-Pin DIP	Z89321/371	Z89371
44-Pin PLCC	Z89321/371	Z89371
44-Pin QFP	N/A ¹	Z89371

NOTES:

1. A third-party adapter tower is required for this configuration. Consult ZiLOG Customer Service for more information.
2. The complete, up-to-date list of all supported devices is specified in the ZiLOG Data Sheet (DS) document shipped with ZiLOG's Order Acknowledgment form. The DS is also available from the ZiLOG Bulletin Board Service (ZBBS) and the home page at www.zilog.com (see Appendix A for additional information).

GUI-SUPPORTED COMPILER, ASSEMBLER FORMATS

The Emulator GUI supports object (binary or Intel hex) code files produced by the ZiLOG Macro Cross Assembler (ZMASM), Production Languages Corporation (PLC)¹ assembler, 2500AD, and other third-party development tool companies.

HARDWARE SPECIFICATIONS

Operating Conditions

Operating Temperature: 20°C ±10°C

Emulation Speed: 1-MHz Internal Clock (Minimum)

20-MHz Internal Clock (Maximum)

Operating Humidity: 10-90% RH (Noncondensing)

Dimensions

0.5 in. (Height) x 4.875 in. (Width) x 7.125 in. (Length)

Serial Interface

RS-232C @ 9600, 19200 (Default), 28800, or 57600 Baud Rates

Emulation Memory

Maximum: 64K Words

Number of Breakpoints

Maximum: 256

1. Production Languages Corporation (PLC) may be reached by telephone at (817) 367-6030 or online at info@plcorp.com.

HOST COMPUTER

Minimum Requirements

IBM PC (or 100-percent compatible) 486-Based Machine

33 MHz

4-MB RAM

VGA Video Adapter

Hard Disk Drive (3.0 MB free space)

3.5-inch, High-Density (HD) Floppy Disk Drive

RS-232 COM Port

Mouse or Pointing Device

Microsoft Windows 3.1

Recommended

The following changes to the Minimum Requirements are recommended for increased performance:

486- or Pentium-Based Machine

66 MHz (or Faster)

8 MB of RAM (or More)

SVGA Video Adapter

Color Monitor

Printer

Microsoft Windows 95

KIT CONTENTS

The emulator kit contains one of each of the following items:

- PC Assembly, Z8932102ZEM DSP Emulation Board
- Cable Assembly, 9-CKT, RS232, 6 Ft.
- Power Supply, AC MOD, 9VCD, 1.2A
- DSP Graphical User Interface (GUI) Software
- ZiLOG Macro Cross Assembler (ZMASM)/ZiLOG Developer System (ZDS) Software
- ZiLOG Macro Cross Assembler (ZMASM) User's Manual
- ZiLOG Macro Cross Assembler (ZMASM) License Agreement
- Z8932102ZEM User's Manual
- Product Information, ZDS For Windows 95
- Pprogramming Adapter for 44-PLCC
- Progoramming Adapter for 44-PQFP
- Emulation Pod for 44-PLCC
- Emulation Pod for 40-DIP



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CHAPTER 2

SET-UP AND INSTALLATION

SOFTWARE INSTALLATION

Software for the Emulator is stored on two sets of diskettes:

1. DSP GUI
2. ZiLOG Macro Cross Assembler (ZMASM) with ZiLOG Developer Studio (ZDS)

NOTE: Refer to the README.TXT file on each diskette. Text (.TXT) files are easily accessed by using the Microsoft Windows Notepad program.

The ZiLOG Macro Cross Assembler (ZMASM) ver. 2.10 is the basis for the ZiLOG 16-bit development environment for Windows 3.1. The ZiLOG Developer Studio (ZDS) version 1.00 is the 32-bit development environment which runs under Windows 95 and includes ZMASM version 2.10. The DSP GUI provides the interface screens that are customized for use with the Z8932102ZEM emulator.

ZMASM/ZDS Installation

If you are installing the ZiLOG Macro Cross Assembler (ZMASM) or ZiLOG Developer Studio (ZDS), run the installation program from the diskette *before* installing the GUI diskette. You may choose to use another assembler, but the sample session assumes the ZiLOG assembler has been installed.

ZDS is available only for the Windows 95 environment.

Under Windows 95, a choice is offered to the user to install either ZDS, that includes ZMASM, or to install ZMASM only.

NOTES:

1. The ZMASM/ZDS is a licensed product; it is not sold. Before opening the envelope containing the software, carefully read the Software License and Limited Warranty Agreement.
2. The installation procedure can be run before creating the installation directory.
3. To ensure that you receive proper notification of updates to this product, please fill out and return the enclosed ZiLOG Registration Card.

To install ZMASM/ZDS, perform the following steps:

1. Select the Run command from the File menu under Program Manager (Windows 3.1), or under the Start button (Windows 95).
2. Insert the ZMASM 2.10 (w/ ZDS 1.00) Disk 1 into drive A (or drive B, if appropriate).
3. Type a:\setup and press ENTER. (Type b:\setup if drive B is used.)
A dialog box prompts you for the directory to install the software into. In Windows 3.1, the default path is C:\ZMASM_2.10. In Windows 95, the default path is C:\Program Files\ZDS_1.00 for ZDS. For the ZMASM-only installation, the default path is C:\Program Files\ZMASM_2.10. The setup program copies the files into the target directory.
4. Follow all on-screen instructions.
In Windows 3.1, a ZMASM program group icon is placed on the desktop. In Windows 95, a ZiLOG Developer Studio entry is placed on the Programs menu under Start.
5. Remove the diskette and store in a safe place when installation is complete.

DSP ICEBOX GUI Installation

To install DSP ICEBOX™ GUI under Windows, perform the following steps:

1. Select the Run command from the File menu under Program Manager (Windows 3.1), or under Start (Windows 95).
2. Insert the DSP GUI S/W Disk 1 into drive A (or drive B, if appropriate).
3. Type a:\setup and press ENTER. (Type b:\setup if drive B is used.)
A dialog box prompts you for the directory to install the software into.
4. Follow all on-screen instructions.
The setup program copies the files into the target directory, creating a DSP ICEBOX GUI program group icon in the Windows environment.
5. Remove and store all diskettes in a safe place when installation is complete.

Program Uninstallation

Uninstaller facilities are created during the installation of ZMASM/ZDS and DSP ICEBOX GUI. In Windows 3.1, an Uninstall icon is a member of each program group. In Windows 95 an entry is created in the submenu under Programs.

The Uninstall facility should be utilized when removing ZMASM/ZDS and DSP ICEBOX GUI from your PC to properly restore the Windows operating environment.

HARDWARE INSTALLATION

Before installing the hardware, refer to Figure 2-1 for a view of the emulator; Figure 2-2 provides a diagram for connecting the emulator to your PC and power supply.

NOTE: Proper functioning of the emulator depends upon proper installation and running of the GUI software on your PC.

Installation Procedure

This section provides the quick method of installing the hardware utilizing VDC wall-adaptor power supply.

1. Connect the serial cable to the PC.
2. If you are doing in-circuit emulation, connect the emulator to your target board.
3. Set up the oscillator and option jumpers.
4. Refer to the “Electrical Safeguards” page of this manual.
5. Plug a 9.0 VDC 1.2 Amp Wall Adaptor to the P11 label +9.0V DC on the Z8932102ZEM.
6. Set the power switch to ON.

Serial Cable Connection

Locate the serial cable. Connect the male end to the female connector, and the female end to either the COM1, COM2, COM3, or COM4 connector of your PC.

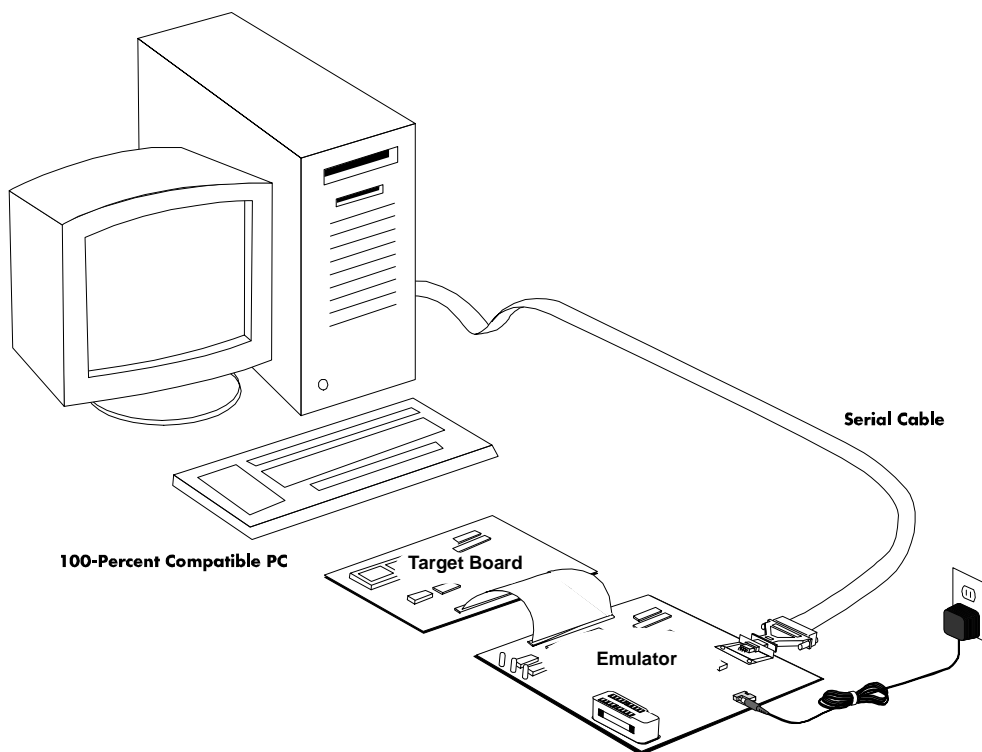
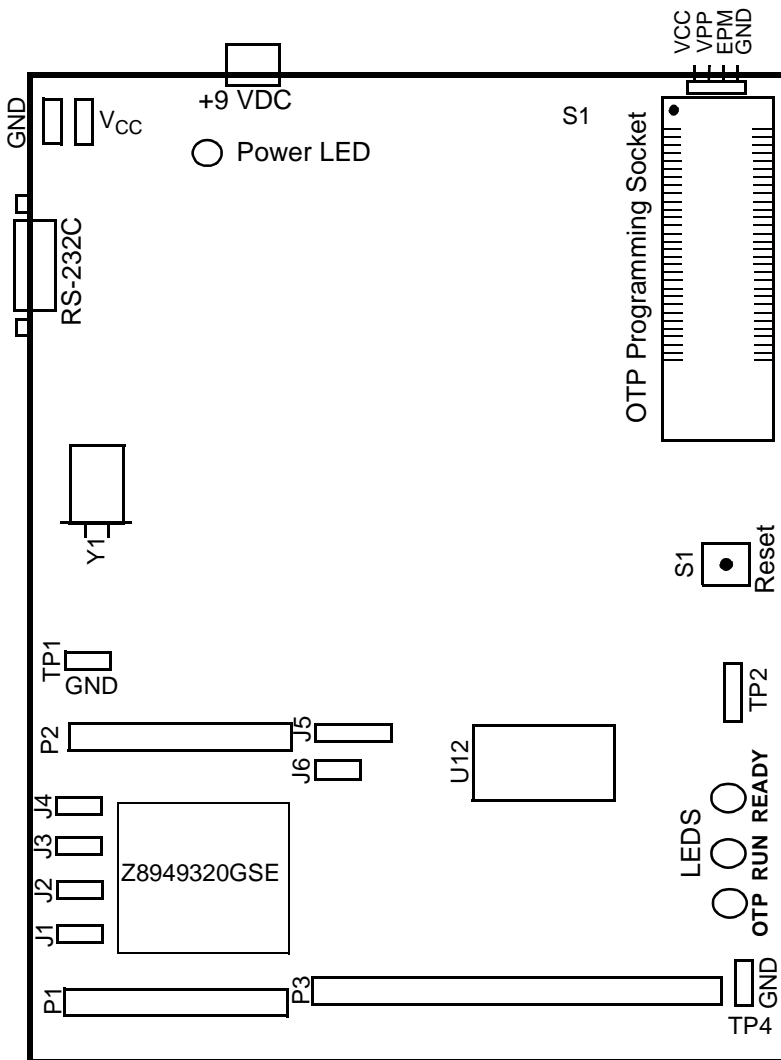


Figure 2-1. Hook-Up Diagram

Jumper Locations**Figure 2-2. Option Jumper Locations**

NOTE: Refer to the following tables for information on how to set up these jumpers for your specific emulator model before proceeding to the next installation steps.

Table 2-1. Jumper Option Settings

Jumper	Position	Description
J1	1-2 2-3 3-4 4-5* 5-6	FS0 drives the user's target board. FS0 drives the on-board CODEC. Reserved FS1 drives the on-board CODEC. FS1 drives the user's target board.
J2	1-2 2-3*	Transmit data is connected to the user's target board. Transmit data is connected to the on-board CODEC.
J3	1-2 2-3*	Receive data is supplied by the user's target board. Receive data is supplied by the on-board CODEC.
J4	Open Closed*	External Halt is Active. External Halt is Not Active.
J5	Open* Closed	External data bus is Enabled. External data bus is Disabled.
J6	Open Closed*	Break logic is Disabled. Break logic is Enabled (Emulation mode).
J7	Open Closed*	Emulation mode is Disabled. Emulation mode is Enabled.
J8	Open* Closed	Normal mode is Enabled. Reserved
J9	1-2* 2-3 3-4 4-5 5-6	DSP clock source is the Oscillator, CLKI. Reserved Reserved DSP clock source is the Oscillator divided by 2, DSP_CLKII. DSP clock source is the User clock, U_CLK.
J10	1-2 2-3*	Oscillator input divided by 2 is V_{CC} (not active - power save). Oscillator input divided by 2 is the 20-MHz Oscillator.
J11	1-2 2-3*	CODEC data format is A-law. CODEC data format is μ -law.
J12	Open* Closed	The target board has its own V_{CC} . The target board does not have its own V_{CC} .
Note: The * designates the default setting when shipped.		

Option Jumpers Setup

Selected option jumpers are accessible on top of the emulator board (see Figure 2-2). The option jumpers allow you to configure things like how the clock pin of the emulated DSP is connected. The clock for the emulated DSP can be provided from an oscillator in the emulator or from a TTL-level clock on your design. For electrical reasons, it cannot be provided from the oscillator on your design until you are done using the emulator and have done one of two things: 1) programmed an EPROM or OTP DSP and installed it in your design, or 2) installed a Masked-ROM DSP in your design.

NOTE: Each emulator is shipped with a 20-MHz oscillator. If you want another rate, remove the oscillator and install a different one, within the frequency specifications of the device in use. ZiLOG, however, does not guarantee operation with oscillators other than the supplied model.

Power Up

If anything unusual (such as unexpected sounds and smells) occurs the first time you turn the power supply ON, quickly turn the power switch OFF, and check your connections. Check the 9V DC wall adapter, and ensure that the adapter has the rated 1.2A current.

After power-up, press the RESET button to reset the Z89321-02 emulator. Pressing the RESET button avoids bus contention on the I/O lines.

Power Down

When powering down, perform the following steps:

1. Power down the target application board (if it has its own power supply).
2. Power down the emulator.

NOTE: Refer to the complete “Electrical Safeguards” page of this manual.

EMULATOR OPERATION

Resetting

Pressing the RESET button on the emulator resets the state of the target device and much of the status that you establish using the GUI. For example, it resets the emulated ROM size and clears all breakpoints. If your application board drives the **RESET** pin of the DSP socket Low, the target DSP is reset. For example, the Program Counter is set to the address set by the reset vector in the code, but most of the other GUI and emulator settings are not affected.

NOTE: After reset, you must wait until the Ready LED is OFF before starting the GUI software. (Refer to “LED Operation” section, which is later in this chapter.

WARNING!

You must always press the RESET button on the emulator *before* selecting the GUI icon or program entry.

ROM Size

The Z89321 emulator features selectable ROM sizing. Up to 64K of code memory is available for program development on the emulator. Then, during a later stage of project development, the user can shrink the program to fit in to the 4K of code memory on the chip.

Breakpoint Implementation

The emulator bases its breakpoint facility completely on addresses, rather than on inserting special Trap instructions into the program, which means you can set breakpoints in RAM on your target board. The emulator uses static RAM for each cycle emitted by the ICE chip during code execution to implement the breakpoint function in hardware.

LED Operation

Table 2-2. LED Assignments

LED	Indication	Description
Power	On Off	Power is ON. Power is OFF.
Ready	Off On Blink	Communicating in Bisync Mode and waiting for command. Communicating in ASCII Mode or executing Bisync command. Emulator is self-testing.
Run	On Off	Running user code. Not running user code.
OTP	On Off	Programming OTP. Not programming OTP.



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CHAPTER 3

SUMMARY OF MENUS, COMMANDS, AND OPERATIONS

MAIN MENU

<u>F</u> ile	<u>V</u> iew	<u>O</u> TP!	<u>C</u> onfiguration	<u>H</u> elp
--------------	--------------	--------------	-----------------------	--------------

The Main Menu window of the emulator graphical user interface (GUI) is displayed after the GUI program is started. The following menu items can be accessed from this Main Menu:

- File
- View
- OTP!
- Configuration
- Help

Each of these items, along with all windows, subset menus, menu items, commands, and operations, are summarized in the following sections of this chapter.

NOTE: Consult ZiLOG's ZBBS or Internet site at www.zilog.com to obtain the latest released version of DSP GUI Emulator software.

FILE MENU

<u>F</u> ile	<u>V</u> iew	<u>Q</u> TP!	<u>C</u> onfiguration	<u>H</u> elp
<u>O</u> pen Session...				
<u>S</u> ave Session...				
<u>D</u> ownload Application...				
Download DSP RAM <u>0</u> ...				
Download DSP RAM <u>1</u> ...				
Download DSP <u>C</u> ode Memory...				
Download DSP External <u>M</u> emory...				
Load <u>S</u> ymbol...				
<u>U</u> pload DSP RAM <u>0</u> ...				
Upload DSP RAM <u>1</u> ...				
<u>U</u> pload DSP Code Memory...				
Upload DSP <u>E</u> xternal Data Memory...				
<u>E</u> xit		Alt + F4		

Open Session

The `Open Session` menu item provides a facility for loading a previously saved session.

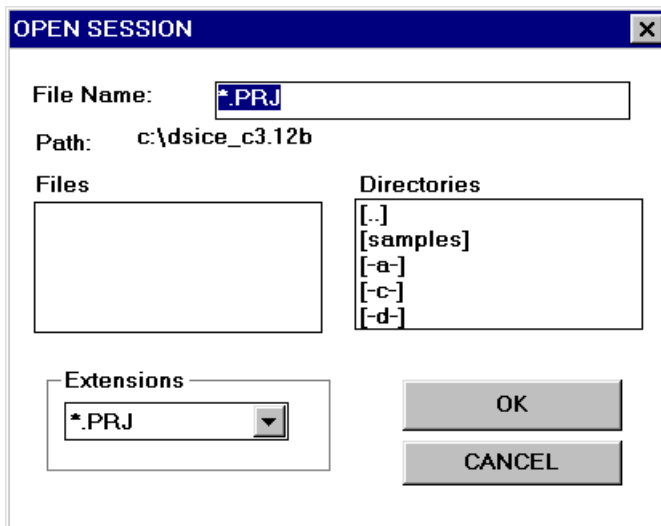


Figure 3-1. Open Session Dialog Box

Save Session

Selecting `Save session` saves the information about the position of the opened windows, downloaded code file, font size used, and debug flags such as `Trace` and `Animation`. Not all windows reappear upon reloading of a saved session.

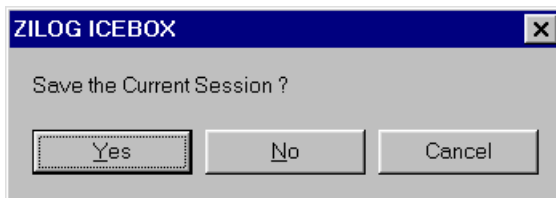


Figure 3-2. ZiLOG ICEBOX (Save Session) Dialog Box

Download Application

The Download Application menu item enables you to download an application object file to the Code Memory of the ICEBOX. Type the path and name or use the **Browse** button to locate and select your file. Two object formats are supported for output from ZMASM or the PLC compiler. In addition, memory can be cleared prior to downloading.

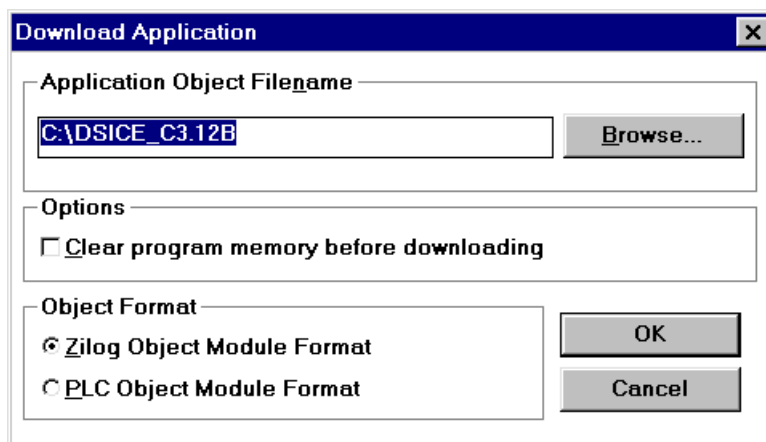


Figure 3-3. Download Application Dialog Box

Download DSP RAM0

The Download DSP RAM0 menu item enables memory to be filled with the values contained in an Intel hex or binary file. The Address Format section of the Download To DSP RAM0 window designates the format of the contents of the file being downloaded. Memory can also be padded with FFFFh or 0000h.

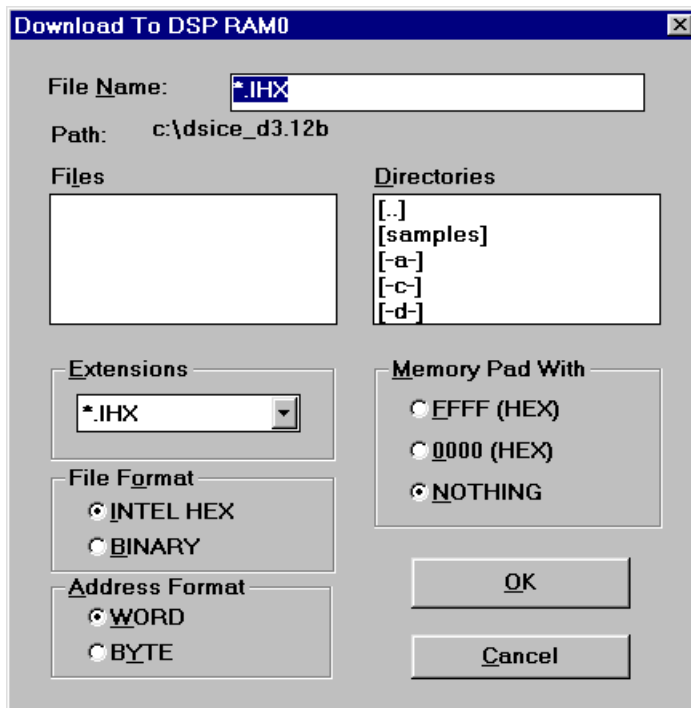


Figure 3-4. Download To DSP RAM0 Dialog Box

Download DSP RAM1

The Download DSP RAM1 menu item enables memory to be filled with the values contained in an Intel hex or binary file. The Address Format section of the Download To DSP RAM1 window designates the format of the contents of the file being downloaded. Memory can also be padded with FFFFh or 0000h.

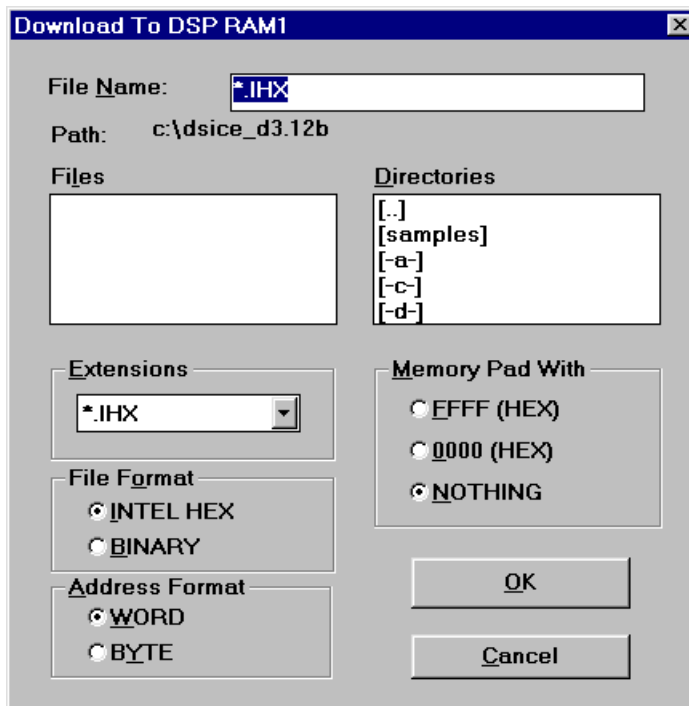


Figure 3-5. Download To DSP RAM1 Dialog Box

Download DSP Code Memory

The Download DSP Code Memory menu item enables you to download Intel hexadecimal (hex) or binary format code to Code Memory. The Address Format section of the Download to DSP Code Memory window designates the format of the contents of the file being downloaded. Memory can also be padded with FFFFh or 0000h.

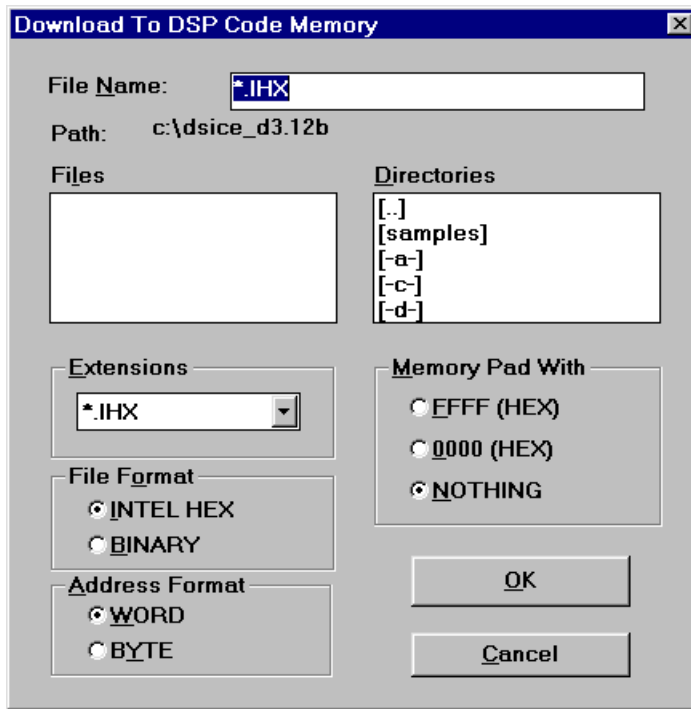


Figure 3-6. Download To DSP Code Memory Dialog Box

Download DSP External Data Memory

The Download to DSP External Data Memory menu item enables you to download Intel hexadecimal (hex) or binary format code to External Data Memory. The Address Format section of the Download To External Data Memory window designates the format of the contents of the file being downloaded. Memory can also be padded with FFFFh or 0000h.

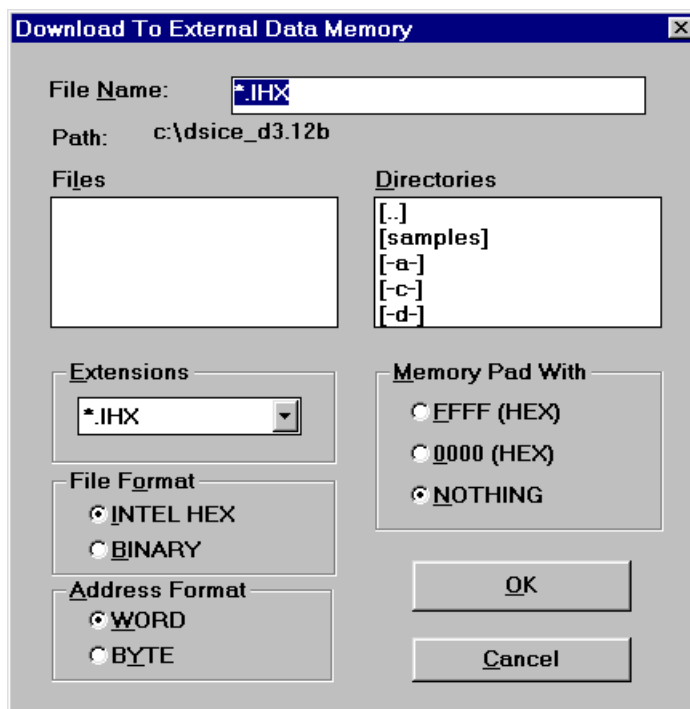


Figure 3-7. Download To External Data Memory Dialog Box

Load Symbol

The Load Symbol menu item enables the selection of a symbol during debugging. The value of corresponding labels and constants in your program code is displayed. This item is available only when the Z89321 Debug window is displayed.

Upload DSP RAM0

The Upload DSP RAM0 menu item enables the contents of memory bank0 to be saved in an object file. Three formats are available for storing the data: binary, Intel hex, and disassembly. The entire contents of the bank are stored when the Save All button is activated. A portion of the contents is stored when numbers are entered in the Start Address and End Address fields, and the Save button is activated.

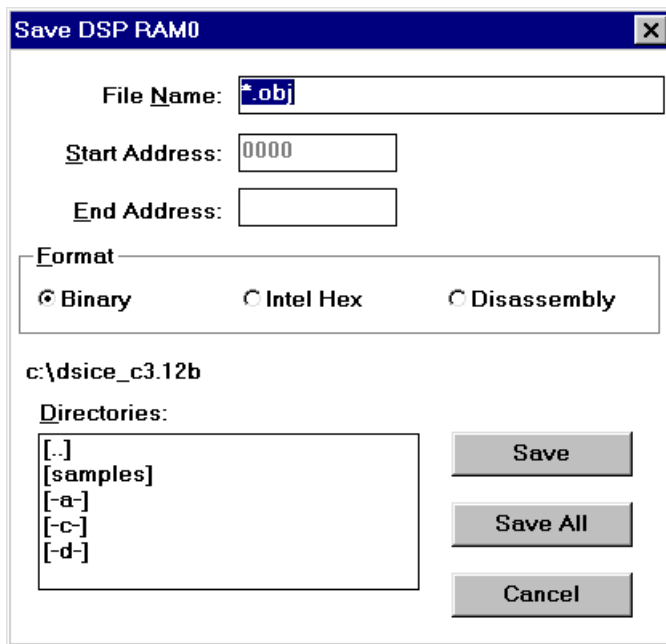


Figure 3-8. Save DSP RAM0 Dialog Box

Upload DSP RAM1

The Upload DSP RAM1 menu item enables the contents of memory bank1 to be saved in an object file. Three formats are available for storing the data: binary, Intel hex, and disassembly. The entire contents of the bank are stored when the Save All button is activated. A portion of the contents is stored when numbers are entered in the Start Address and End Address fields, and the Save button is activated.

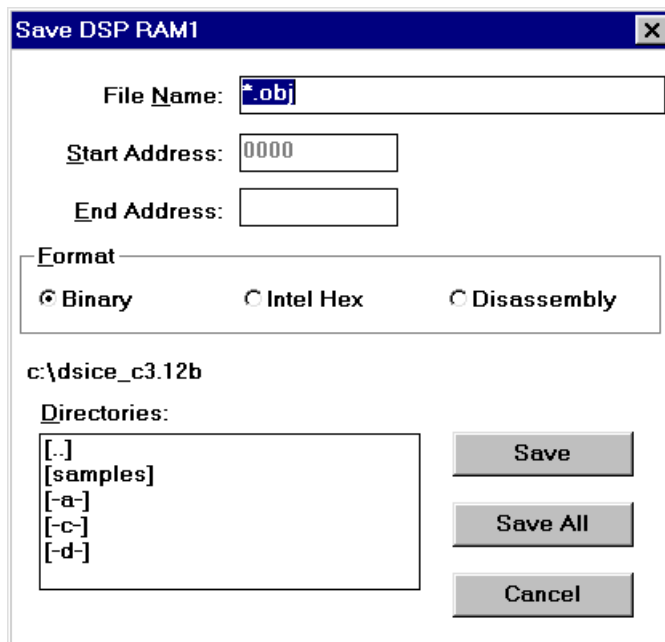
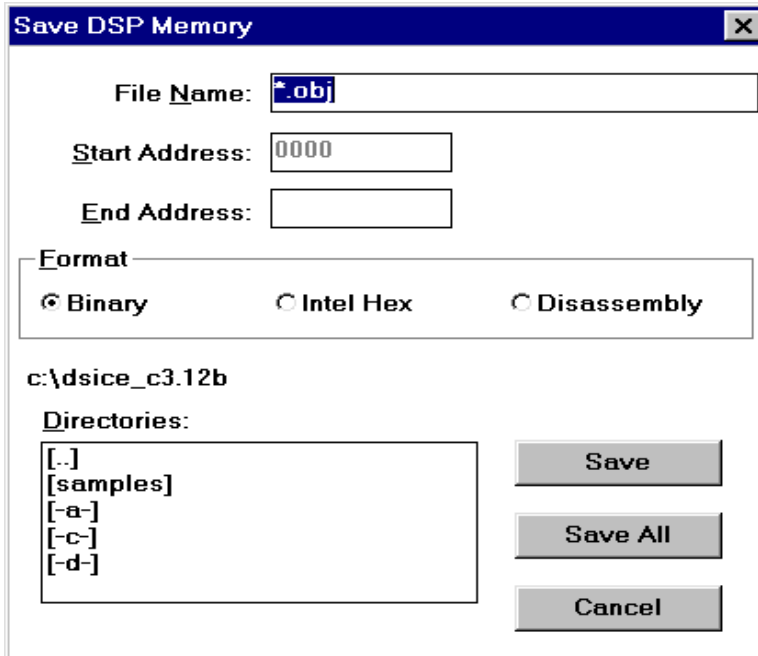


Figure 3-9. Save DSP RAM1 Dialog Box

Upload DSP Code Memory

The Upload DSP Code Memory menu item enables the contents of code memory to be saved in an object file. Three formats are available for storing the data: binary, Intel hex, and disassembly. The entire contents of code memory are stored when the Save All button is activated. A portion of the contents is stored when numbers are entered in the Start Address and End Address fields, and the Save button is activated.

This function may be used for sessions involving patched code.



The dialog box is titled "Save DSP Memory" and contains the following fields and controls:

- File Name:** A text field containing "*.obj".
- Start Address:** A text field containing "0000".
- End Address:** An empty text field.
- Format:** A section with three radio buttons: ☒ Binary, ☐ Intel Hex, and ☐ Disassembly.
- Directory:** A text field containing "c:\dsice_c3.12b".
- Directories:** A list box containing the following entries: [.-], [samples], [-a-], [-c-], and [-d-].
- Buttons:** Three buttons are located on the right side: "Save", "Save All", and "Cancel".

Figure 3-10. Save DSP Memory Dialog Box

Upload DSP External Data Memory

The Upload DSP External Data Memory menu item enables the contents of external data memory to be saved in an object file. Three formats are available for storing the data: binary, Intel hex, and disassembly. The entire contents of external data memory are stored when the Save All button is activated. A portion of the contents is stored when numbers are entered in the Start Address and End Address fields, and the Save button is activated.

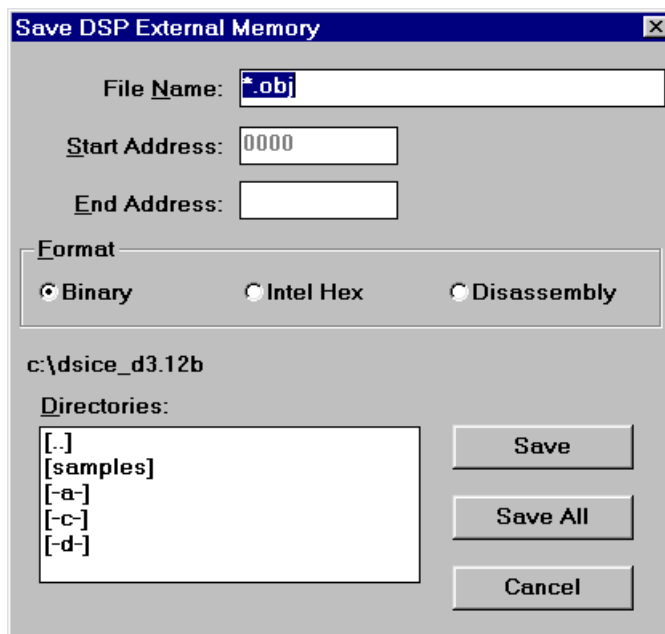


Figure 3-11. Save DSP External Memory Dialog Box

Exit

The Exit menu item enables you to leave the emulator GUI. The ZiLOG ICEBOX confirmation window offers the message, “Save the Current Session?”, and the options Yes, No, and Cancel.

VIEW MENU

File	View	OTP!	Configuration	Help
	Registers			
	RAM Bank0			
	RAM Bank1			
	DSP Code Memory			
	DSP External Data Memory			
	Debug			
	Source			
	Output			

Registers

The Registers menu item initiates the display of the Z89321 Registers dialog box. That window contains the contents of all internal and external registers, RAM pointer registers, data registers, status registers and flags, and stacks.

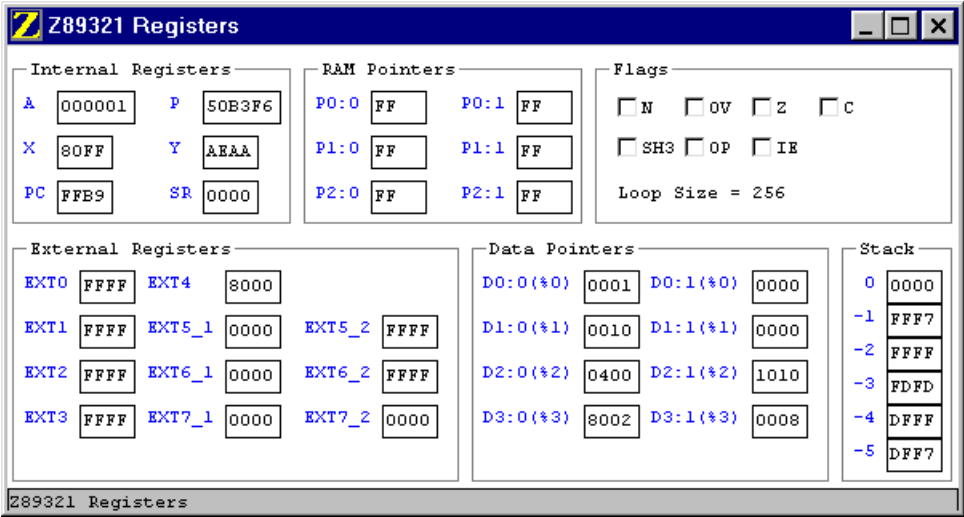


Figure 3-12. Z89321 Registers Dialog Box

When the Z89321 Registers window is displayed, the following menu bar is displayed:

<u>F</u> ile	<u>V</u> iew	<u>Q</u> TP!	<u>F</u> ont <u>S</u> ize	<u>W</u> indow	<u>H</u> elp
--------------	--------------	--------------	---------------------------	----------------	--------------

Font Size

You can choose from a number of font sizes: Point 6 to Point 12.

<u>F</u> ile	<u>V</u> iew	<u>Q</u> TP!	<u>F</u> ont <u>S</u> ize	<u>W</u> indow	<u>H</u> elp
			Point <u>6</u> Point <u>8</u> Point <u>10</u> Point <u>12</u>		

Window

<u>F</u> ile	<u>V</u> iew	<u>Q</u> TP!	<u>F</u> ont <u>S</u> ize	<u>W</u> indow	<u>H</u> elp
				<u>C</u> ascade Shift+F5 <u>T</u> ile Shift+F4 Arrange <u>I</u> con Close <u>A</u> ll	

The Cascade, Tile, and Arrange Icon items perform standard Windows functions.

Close All displays the ZiLOG ICEBOX confirmation window which offers the message, “Save the Current Session?”, and the options Yes, No, and Cancel (see Figure 3-2).

RAM Bank0

The RAM Bank0 menu item activates the Z89321 RAM Bank0 window and enables the viewing and editing of the contents of internal data bank0.

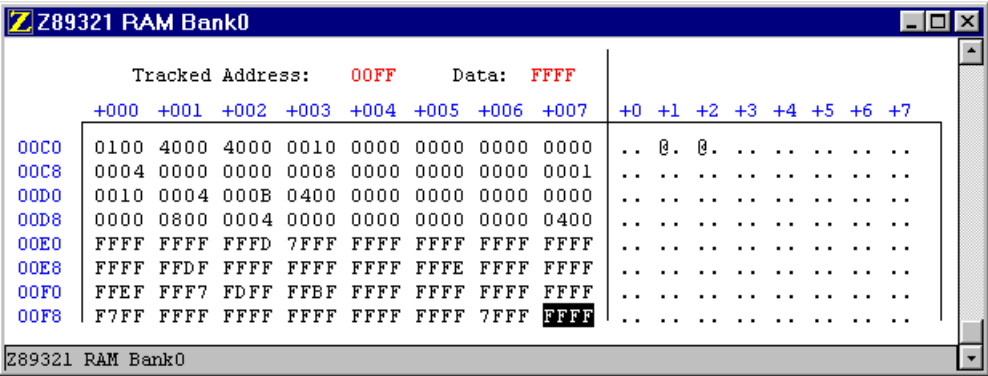


Figure 3-13. Z89321 RAM Bank0 Window

When the Z89321 RAM Bank0 window is displayed, the following menu bar is displayed:

<u>F</u> ile	<u>V</u> iew	<u>Q</u> TP!	<u>E</u> dit	<u>T</u> racking	<u>F</u> ont <u>S</u> ize	<u>W</u> indow	<u>H</u> elp
--------------	--------------	--------------	--------------	------------------	---------------------------	----------------	--------------

Edit

<u>F</u> ile	<u>V</u> iew	<u>Q</u> TP!	<u>E</u> dit	<u>T</u> racking	<u>F</u> ont <u>S</u> ize	<u>W</u> indow	<u>H</u> elp
			<u>F</u> ill...				
			<u>C</u> lear All				
			Start <u>A</u> ddress...				

Fill

The Fill menu item enables you to assign a value to a specified range of memory addresses. The Memory Fill dialog box has entry fields for Start Address, End Address, and Fill Value. The Radix field specifies the format, hexadecimal or decimal, of the entry in the Fill Value field.

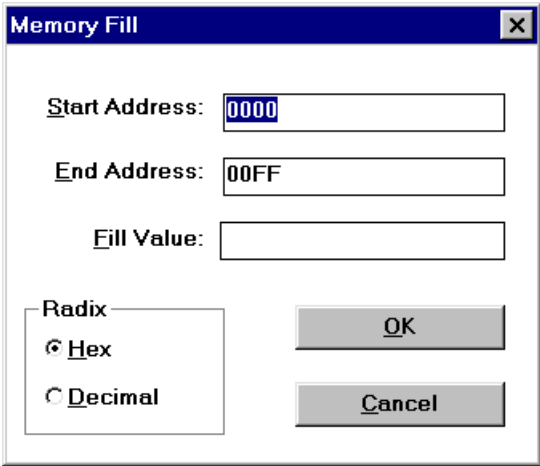


Figure 3-14. Memory Fill Dialog Box

Clear All

The Clear All menu item provides the option of filling Z8 Code Memory with the 0000h or FFFFh value. To exit the choice list without any changes, press the Escape key.

File	View	QTP!	Edit	Tracking	Font Size	Window	Help
			Fill...				
			Clear All	With 0000			
			Start Address...	With FFFF			

When the With 0000 or With FFFF menu item is selected, a confirmation window is displayed.

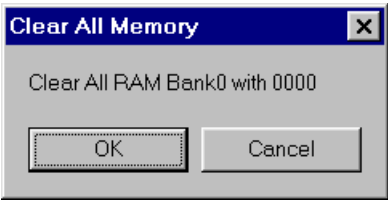


Figure 3-15. Clear All Memory Confirmation Window

Start Address

The `Start Address` menu item provides a facility for jumping the cursor in the Z89321 RAM Bank0 window to the location entered in the `Start Address` field. The `Radix` field specifies the format, hexadecimal or decimal, of the entry in the `Start Address` field.

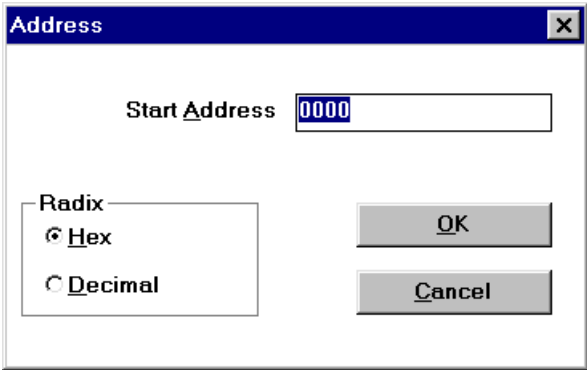


Figure 3-16. Address Dialog Box - Start Address

Tracking

Selecting `Tracking` from the menu, displays the `Absolute Address`, `Pointer Register P0:0`, `Pointer Register P1:0`, and `Pointer Register P2:0` menu items.

File	View	QTP!	Edit	Tracking	Font Size	Window	Help
				Absolute Address			
				Pointer Register P0:0			
				Pointer Register P1:0			
				Pointer Register P2:0			

Absolute Address: You can track to a register by its absolute address by entering the absolute address value in the dialog box that appears when Absolute Address is selected from the Tracking menu.

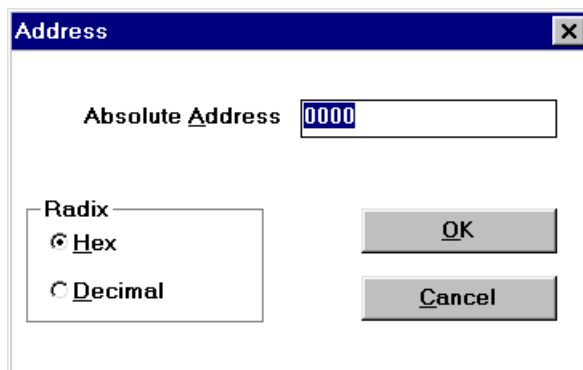


Figure 3-17. Address Dialog Box - Absolute Address

Pointer Register P0:0: You can track to the contents of pointer register P0:0.

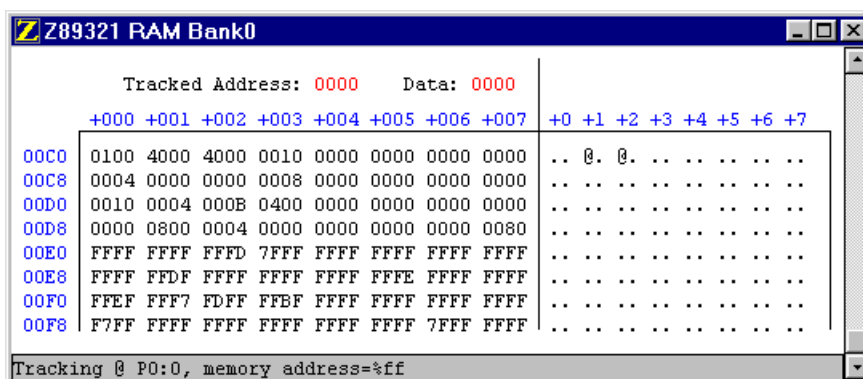


Figure 3-18. Tracking Pointer Register P0:0

Pointer Register P1:0: You can track to the contents of pointer register P1:0.

Z89321 RAM Bank0

Tracked Address:								Data:							
+000	+001	+002	+003	+004	+005	+006	+007	+0	+1	+2	+3	+4	+5	+6	+7
00C0	0100	4000	4000	0010	0000	0000	0000	0000	0000	0000	0000	0000	0000	0000	0000
00C8	0004	0000	0000	0008	0000	0000	0000	0000	0000	0000	0000	0000	0000	0000	0000
00D0	0010	0004	000B	0400	0000	0000	0000	0000	0000	0000	0000	0000	0000	0000	0000
00D8	0000	0800	0004	0000	0000	0000	0000	0000	0000	0000	0000	0080	0000	0000	0000
00E0	FFFF	FFFF	FFFD	7FFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF
00E8	FFFF	FFDF	FFFF	FFFF	FFFF	FFFE	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF
00F0	FFEF	FFF7	FDFF	FFBF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF
00F8	F7FF	FFFF	FFFF	FFFF	FFFF	FFFF	7FFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF	FFFF

Tracking @ P1:0, memory address=%ff

Figure 3-19. Tracking Pointer Register P1:0

Pointer Register P2:0: You can track to the contents of pointer register P2:0.

Z89321 RAM Bank0

	Tracked Address: 00FF								Data: FFFF							
	+000	+001	+002	+003	+004	+005	+006	+007	+0	+1	+2	+3	+4	+5	+6	+7
00C0	0100	4000	4000	0010	0000	0000	0000	0000	..	@.	@.
00C8	0004	0000	0000	0008	0000	0000	0000	0000
00D0	0010	0004	000B	0400	0000	0000	0000	0000
00D8	0000	0800	0004	0000	0000	0000	0000	0000
00E0	FFFF	FFFF	FFFD	7FFF	FFFF	FFFF	FFFF	FFFF
00E8	FFFF	FFDF	FFFF	FFFF	FFFF	FFFE	FFFF	FFFF
00F0	FFEF	FFF7	FDFF	FFBF	FFFF	FFFF	FFFF	FFFF
00F8	F7FF	FFFF	FFFF	FFFF	FFFF	FFFF	7FFF	FFFF

Tracking @ P2:0, memory address=%ff

Figure 3-20. Tracking Pointer Register P2:0

When you right-click on your two-button mouse, the following items are displayed:

- Clear All...
- Fill...
- Tracking Absolute Address...
- Download DSP RAM0...
- Upload DSP RAM0...

RAM Bank1

The RAM Bank1 menu item activates the Z89321 RAM Bank1 window and enables the viewing and editing of the contents of internal data bank1.

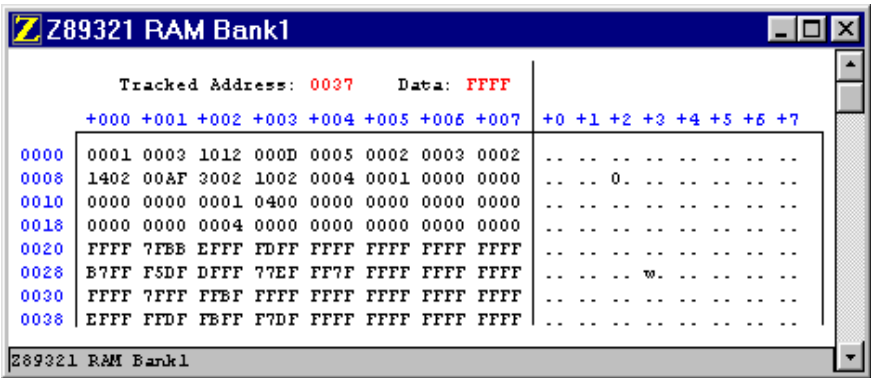


Figure 3-21. Z89321 RAM Bank1 Window

When the RAM Bank1 dialog box is displayed, the following menu bar is displayed:

<u>F</u> ile	<u>V</u> iew	<u>O</u> TP!	<u>E</u> dit	<u>T</u> racking	<u>F</u> ont <u>S</u> ize	<u>W</u> indow	<u>H</u> elp
--------------	--------------	--------------	--------------	------------------	---------------------------	----------------	--------------

Tracking

Selecting Tracking from the menu, displays the Absolute Address, Pointer Register P0:1, Pointer Register P1:1, and Pointer Register P2:1 menu items.

<u>F</u> ile	<u>V</u> iew	<u>O</u> TP!	<u>E</u> dit	<u>T</u> racking	<u>F</u> ont <u>S</u> ize	<u>W</u> indow	<u>H</u> elp
				<u>A</u> bsolute Address			
				Pointer Register <u>P</u> 0:1			
				Pointer Register <u>P</u> 1:1			
				Pointer Register <u>P</u> 2:1			

Absolute Address: You can track to a register by its absolute address by entering the absolute address value in the dialog box that appears when Absolute Address is selected from the Tracking menu (see Figure 3-17).

Pointer Register P0:1: You can track to the contents of pointer register P0:1.

Z89321 RAM Bank

Tracked Address: 0000 Data: 0001

	+000	+001	+002	+003	+004	+005	+006	+007	+0	+1	+2	+3	+4	+5	+6	+7
0008	1402	00AF	3002	1002	0004	0001	0000	0000	0.
0010	0000	0000	0001	0400	0000	0000	0000	0000
0018	0000	0000	0004	0000	0000	0000	0000	0000
0020	FFFF	7FEB	EDFF	FDFF	FFFF	FFFF	FFFF	FFFF
0028	B7FF	F5DF	DFFF	77EF	FF7F	FFFF	FFFF	FFFF	w.
0030	FFFF	7FFF	FEBF	FFFF	FFFF	FFFF	FFFF	FFFF
0038	FFFF	FFDF	FBFF	F7DF	FFFF	FFFF	FFFF	FFFF
0040	0000	0000	0000	0004	0000	0000	0000	0000

Tracking @ P0:1, memory address=%9

Figure 3-22. Tracking Pointer Register P0:1

Pointer Register P1:1: You can track to the contents of pointer register P1:1.

Z89321 RAM Bank

Tracked Address: 00FF

Data: FFFF

	+000	+001	+002	+003	+004	+005	+006	+007	+0	+1	+2	+3	+4	+5	+6	+7
00C0	0000	0000	0000	0000	0040	0000	0000	00000
00C8	4000	0000	0110	0100	0000	0000	0000	0000	0.
00D0	0000	0018	0000	0004	0000	0010	0000	0000
00D8	0010	2000	0040	0000	0000	0000	0000	00000
00E0	FFFF	FFFF	FFF7	FFFF	FFFF	FFFF	FFFF	FFFF
00E8	FDFF	FFFB	FDFF	FF7F	FFFF	FFFF	FFFF	FFFF
00F0	FFFF	7FFF	BFE7	FF5F	FFFF	FFFF	FFFF	FFFF	_
00F8	7EDF	FFFF	FF7F	F77F	FFFF	FFFF	FFFF	FFFF	~.

Tracking @ P1:1, memory address=%ff

Figure 3-23. Tracking Pointer Register P1:1

Pointer Register P2:1: You can track to the contents of pointer register P2:1.

Tracked Address: 00FF Data: FFFF	
	+000 +001 +002 +003 +004 +005 +006 +007 +0 +1 +2 +3 +4 +5 +6 +7
00C0	0000 0000 0000 0000 0040 0000 0000 00000
00C8	4000 0000 0110 0100 0000 0000 0000 0000 0.
00D0	0000 0018 0000 0004 0000 0010 0000 0000
00D8	0010 2000 0040 0000 0000 0000 0000 0000 .. .0
00E0	FFFF FFFF FFF7 FFFF FFFF FFFF FFFF FFFF
00E8	FDFF FFFB FDFF FF7F FFFF FFFF FFFF FFFF
00F0	FFFF 7FFF BFE7 FF5F FFFF FFFF FFFF FFFF
00F8	7EDF FFFF FF7F D77F FFFF FFFF FFFF FFFF ~.

Tracking @ P2:1, memory address=%ff

Figure 3-24. Tracking Pointer Register P2:1

When you right-click on your two-button mouse, the following items are displayed:

- Clear All...
- Fill...
- Tracking Absolute Address...
- Download DSP RAM1...
- Upload DSP RAM1...

DSP Code Memory

The Z89321 DSP Memory window provides a facility for tracking and changing the contents of the code memory registers.

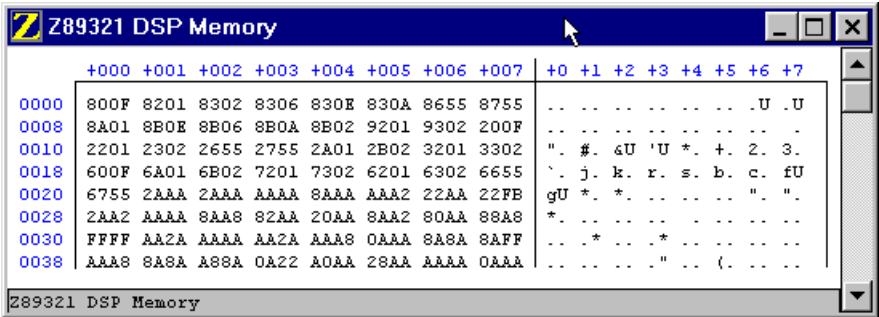


Figure 3-25. DSP Memory Window

When the Z89321 DSP Memory dialog box is displayed, the following menu bar is displayed:

File	View	OTP!	Edit	Tracking	Font Size	Window	Help
------	------	------	------	----------	-----------	--------	------

Tracking

Selecting Tracking from the menu displays the following menu items:

File	View	OTP!	Edit	Tracking	Font Size	Window	Help
				Absolute Address			
				@Register			
				Program Counter			
				@ @ P0:0			
				@ @ P1:0			
				@ @ P2:0			
				@ @ P0:1			
				@ @ P1:1			
				@ @ P2:1			

Absolute Address: You can track to a register by its absolute address by entering the absolute address value in the dialog box that appears when Absolute Address is selected from the Tracking menu (see Figure 3-17).

@Register: You can indirectly track to a register (the contents of which are the address of another register) by entering the value in the At Register dialog box.

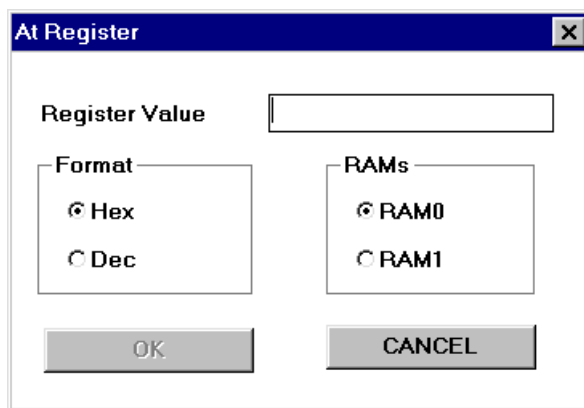


Figure 3-26. At Register Dialog Box

Program Counter: The Program Counter message window reports the current value of the program counter.

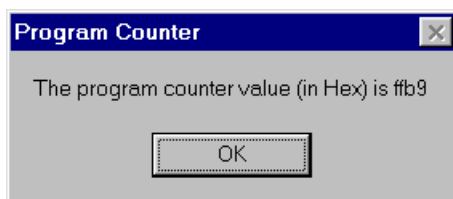


Figure 3-27. Program Counter Window

@@ P0:0: You can track the RAM pointer P0:0 to the DSP Code Memory window.

@@ P1:0: You can track the RAM pointer P1:0 to the DSP Code Memory window.

@@ P2:0: You can track the RAM pointer P2:0 to the DSP Code Memory window.

@@ P0:1: You can track the RAM pointer P0:1 to the DSP Code Memory window.

@@ P1:1: You can track the RAM pointer P1:1 to the DSP Code Memory window.

@@ P2:1: You can track the RAM pointer P2:1 to the DSP Code Memory window.

When you right-click on your two-button mouse, the following items are displayed:

- Clear All...
- Fill...
- Download Application...
- Download DSP Code Memory...
- Upload DSP Code Memory...

Debug

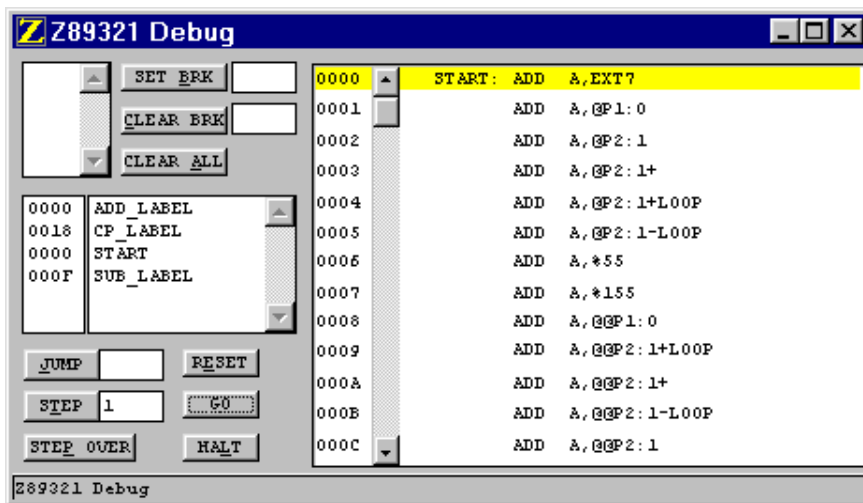


Figure 3-28. Z89321 Debug Dialog Box

The Z89321 Debug window enables you to view the disassembled code, set break points, trace through the code, and perform other debug operations.

Display and Input Fields

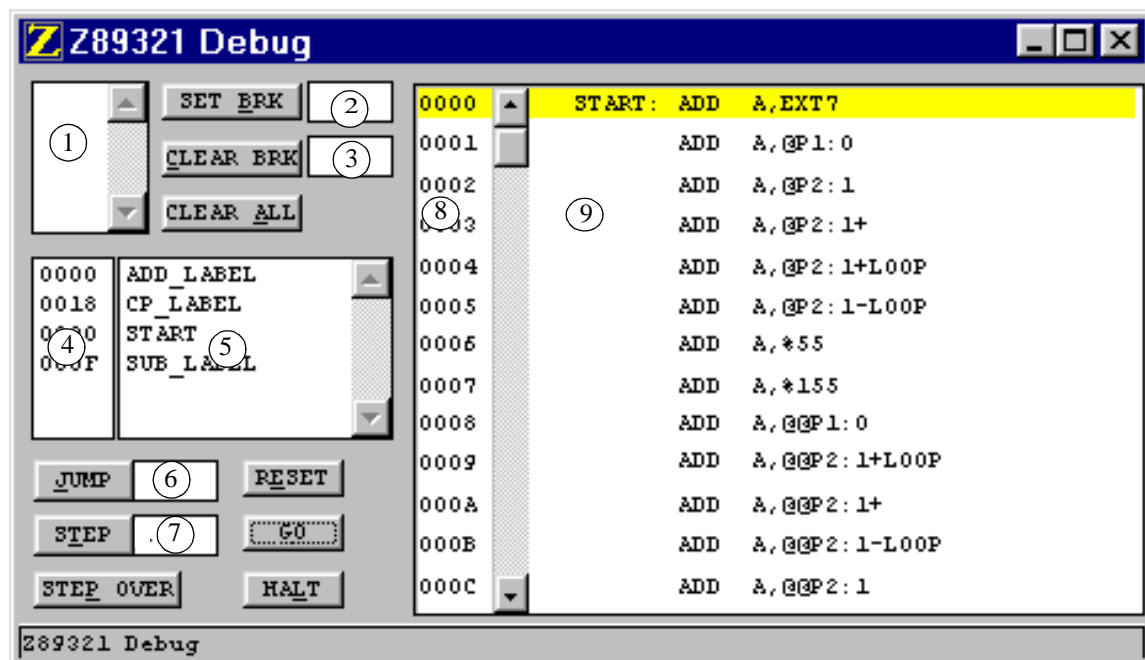


Figure 3-29. Z89321 Debug Fields

The Z89321 Debug dialog box contains the several fields which are numbered for reference in the preceding figure:

Table 3-1. Debug Display and Input Fields

Number	Name	Description
1	Breakpoint List	This display area contains a list of all addresses currently defined as breakpoints.
2	Set Breakpoint Input	This field is an input area for setting a breakpoint. To set a new breakpoint, type the address in this field or click on a line of code in the Code List field (field 9), then click on the SET BRK button. After the button is activated, the address continues to be displayed in the field. The address then appears in the Breakpoint List (field 1).

Table 3-1. Debug Display and Input Fields

Number	Name	Description
3	Clear Breakpoint Input	This field is an input area for deleting a breakpoint. To delete a breakpoint, type the address in this field or place the cursor on the address, then click on the CLEAR BRK button. After the button is activated, the address continues to be displayed in the field. The address is removed from the Breakpoint List (field 1).
4	Symbol Address	This display area contains the address of the symbol to the right of the divider line.
5	Symbol Table	This display area contains the list of currently defined symbols. The symbol table is loaded only when both of the following conditions are fulfilled: 1) A symbol file (. SYM) is loaded in code memory, and 2) The DSP Debug window is selected.
6	Jump	This field is an input area for setting the program counter to the address of a specific line of source code. To jump to an address, type the address in the field, then click on the JUMP button. The address continues to be displayed in the field. The address and line of source code are displayed in the Trace Buffer (field 8) and Code List (field 9) fields.
7	Step Editing	This field is an input area for the number of steps to advance the program counter. The default value is 1. The maximum value is 99999. To single-step through your code, set the value of this field to 1, then click on the STEP button, or click on the GO button.
8	Trace Buffer	This display area contains the address of the line of code displayed on the same line in the Code List field (field 9). Use the vertical scroll bar to scroll up and down.
9	Code List	<p>The Code List contains the disassembled contents of code memory. The field scrolls with the Trace Buffer field. Use the vertical scroll bar to scroll up and down. Selection of a line of code causes the address of that line to be inserted into the SET BRK, CLEAR BRK, and JUMP fields.</p> <p>Code Memory can be change by entering the assembly code directly into the window line by line. After you input a line of assembly code, you must press the RETURN key. The assembled code is then written to Code Memory and immediately reflected in this field.</p> <p>NOTE: Be careful when replacing instructions to avoid corruption of the remaining assembly code.</p>

Buttons

Several buttons are available in the Z89321 Debug window.

Table 3-1. Debug Buttons

Name	Description
CLEAR ALL	Activation of this button clears all breakpoints. All addresses are then removed from the Breakpoint List (field 1).
CLEAR BRK	Activation of this button deletes a breakpoint at the address in the Clear Breakpoint Input field (field 3). The address is then removed from the Breakpoint List (field 1).
GO	Activation of this button starts execution of the DSP program. Execution stops when it hits a breakpoint.
HALT	Activation of this button terminates execution.
JUMP	Activation of this button sets the program counter to the address in the Jump field (field 6). The corresponding line of code is highlighted in the Trace Buffer (field 8) and Code List (field 9) fields.
RESET	Activation of this button causes a jump to the address located at the reset vector location in the code.
SET BRK	Activation of this button sets a breakpoint at the address in the Set Breakpoint Input field (field 2). The address is then added to the Breakpoint List (field 1).
STEP	Activation of this button advances the program counter the number of steps in the Step Editing field (field 7). When the value is set to 1, the STEP button single-steps through the code without causing a RESET .
STEP OVER	Activation of this button advances the program counter a single step. That is, one assembly instruction is executed. When the current instruction is a CALL instruction, subsequent code is executed in real time until the program returns from the subroutine. STEP OVER has the same effect as setting a breakpoint at the instruction immediately following the current one, and executing the GO command.

When the Z89321Debug dialog box is displayed, the following menu is displayed:

File	View	QTP!	Run	FontSize	Window	Help
-------------	-------------	-------------	------------	-----------------	---------------	-------------

Run

<u>F</u> ile	<u>V</u> iew	<u>Q</u> TP!	<u>R</u> un	<u>F</u> ont <u>S</u> ize	<u>W</u> indow	<u>H</u> elp
			<u>T</u> race <u>C</u> ode <u>T</u> race <u>C</u> all <u>A</u> nimate <u>C</u> lear <u>T</u> race <hr/> <u>L</u> og <u>E</u> xecution <u>L</u> og <u>O</u> ptions			

Trace Code: Provides a line-by-line trace capability for tracing all instructions while running the Debug program. When `Trace Code` is selected, `Animate` is also automatically selected and execution is running. See Chapter 4, “Sample Session”, for a sample of tracing activities.

Trace Call: Provides the capability to trace only subroutine calls while running the Debug program. Traced code is displayed in the `Code List` field (field 9) of the Z89321 Debug window. When the `STEP OVER` button is activated and the current instruction is a `CALL` instruction, subsequent code is executed in real time until the program returns from the subroutine. When `Trace Call` is selected, `Animate` is also automatically selected and execution is simulated.

Animate: Animation is a mode where the user can simulate single-step execution through the code. A `GO` command starts execution. The address and instruction of the current and last 99 executed lines of code are displayed in the `Trace Buffer` field (field 8) and the `Trace Buffer` field (field 9) of the Z89321 Debug window.

NOTE: When Animation is activated, execution is not realtime. Single step execution is emulated. To execute code in real time, the `Animation` feature *must* be disabled.

Clear Trace: Clears the contents of the `Trace` field and ends tracing activities.

Log Execution: Enables or disables logging. When enabled, selected registers are logged to the specified file each time emulation is halted, such as at breakpoint or after single-step. See Chapter 4, “Sample Session”, for an example of trace logging.

Log Options: Specifies the registers to log, the format in which to log them, and the name of the log file. Logging may be linear or circular. Linear logging treats the log file as a linear buffer with infinite capacity; circular logging treats the log file as a circular buffer with finite capacity. The circular buffer default capacity is 100 logging actions; however, the number is user-definable in the `Maximum` field. (See Figure 3-30.)

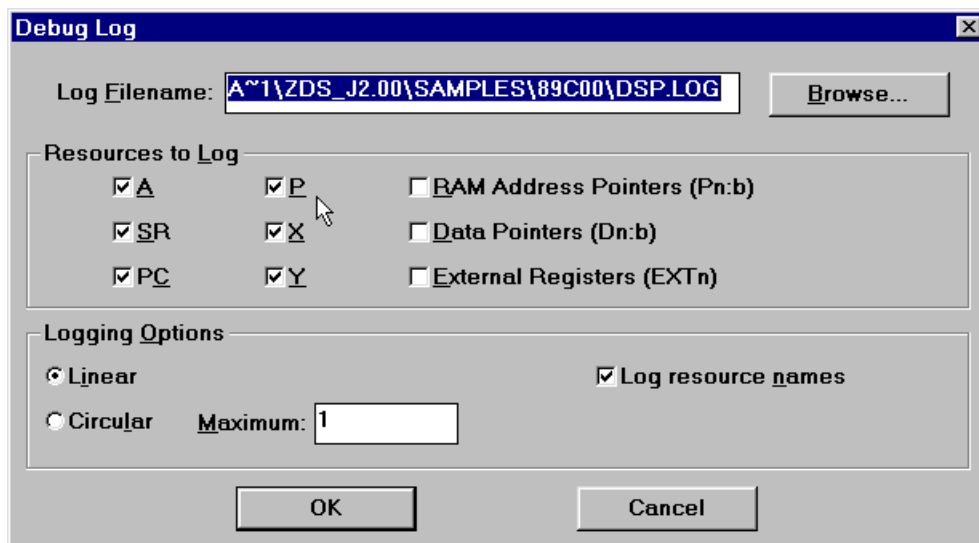


Figure 3-30. Debug Log Window

Resources to Log: The following items are available for logging.

A - A Register (Accumulator)

SR - Status Register

PC - Program Counter

P - P Register (Output of Multiplier, Read Only)

X - X Register (X Multiplier Input)

Y - Y Register (Y Multiplier Input)

RAM Address Pointers (Pn:b)

Data Pointers (Dn:b)

External Registers (EXTn)

For detailed information about these resources and registers, refer to the *Z89321/371 16-Bit Digital Signal Processors Preliminary Product Specification* (DS000500-MOD0698).

Source

The DSP Source dialog box displays the source code when you download a load (.LD) format file using the Download Application dialog box.

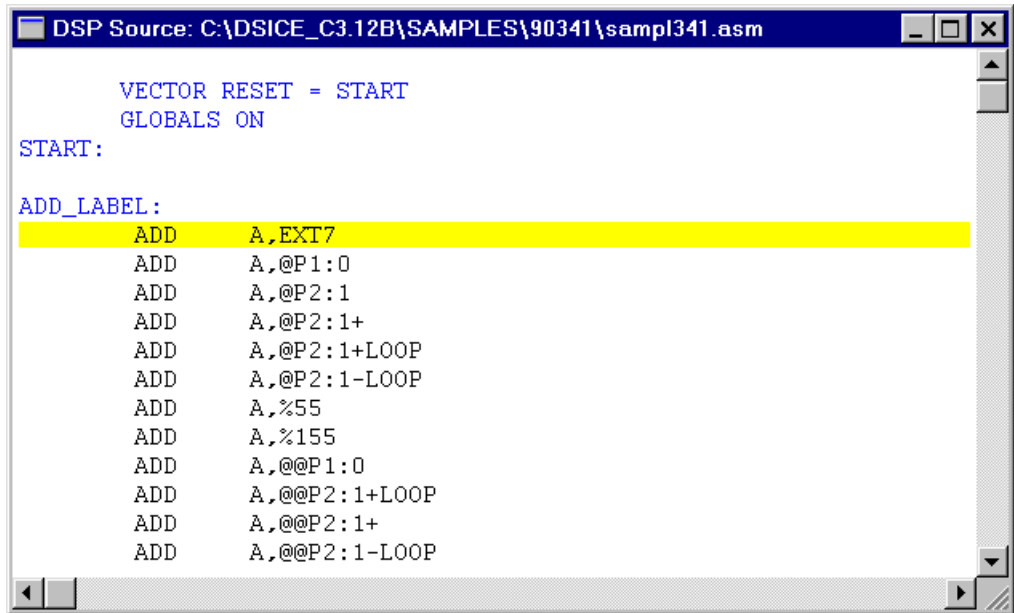


Figure 3-31. DSP Source Window

When the DSP Source window is displayed, the following menu bar is displayed:

<u>F</u> ile	<u>V</u> iew	<u>Q</u> TP!	<u>O</u> ptions	<u>W</u> indow	<u>H</u> elp
--------------	--------------	--------------	-----------------	----------------	--------------

Options

When the Options item is selected, the following item is displayed:

<u>F</u> ile	<u>V</u> iew	<u>Q</u> TP!	<u>O</u> ptions	<u>W</u> indow	<u>H</u> elp
			Source Module...		

Source Module: The Source Module window displays the name of the source assembler module.



Figure 3-32. Source Module Window

Output

The Output window displays the status at file load and at command execution, such as HALT and RESET.

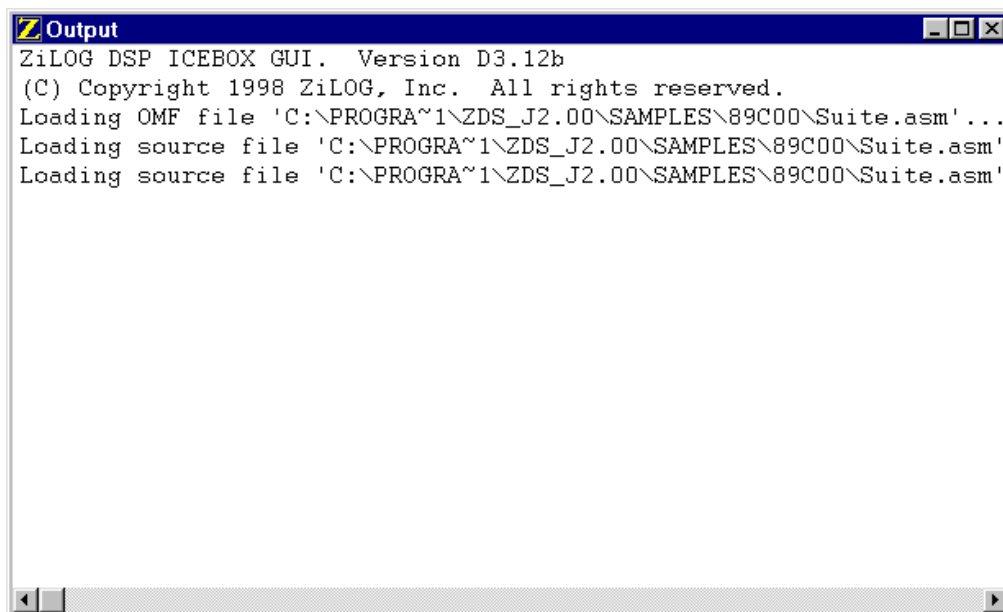


Figure 3-33. Output Window

OTP

This emulator provides OTP programming capabilities. Selecting the OTP! menu item displays the OTP window.

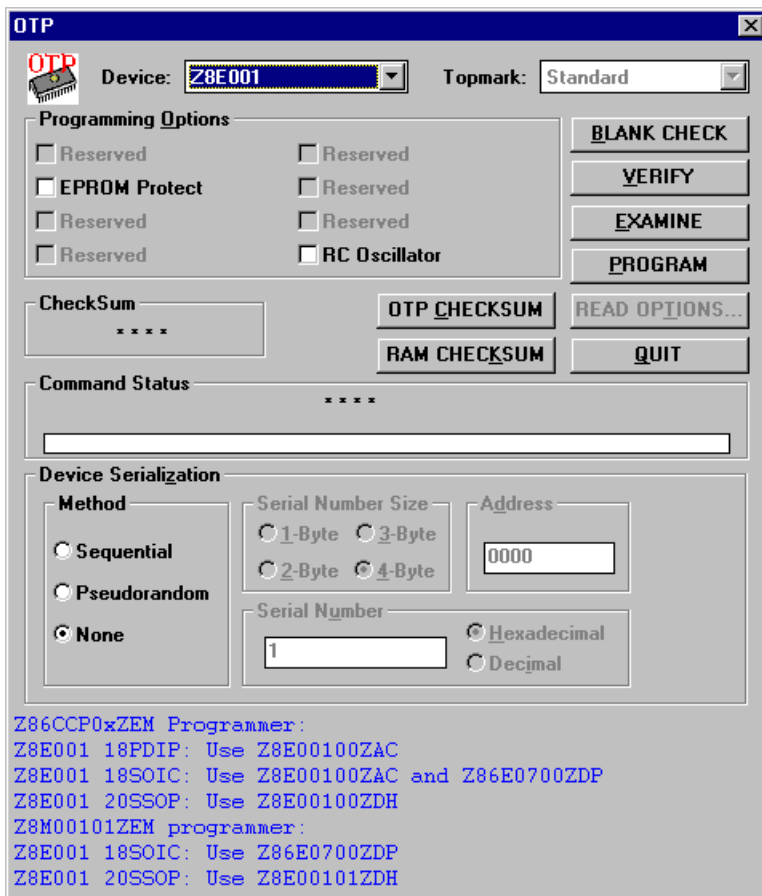


Figure 3-34. OTP Window

OTP ROM

The OTP ROM section of the OTP window sets the method for bit significance in the registers. Three settings are available:

- Word
- High
- Low

Programming Options

Several OTP operations can be performed:

Table 3-1. OTP Operations Functions

Operations	Functions Performed
<u>B</u> LANK CHECK	Checks whether the OTP device is blank.
<u>V</u> ERIFY	Compares the contents of the Code Memory and the OTP device.
<u>E</u> XAMINE	Load the contents of the OTP device into Code Memory.
<u>P</u> ROGRAM	Writes the contents of the Code Memory to the OTP device.
OTP <u>C</u> HECKSUM	Calculates the Checksum of the OTP device only and displays it on the screen.
RAM CHECK <u>S</u> UM	Calculates the Checksum of the RAM and displays it on the screen.
<u>R</u> QM Protect	Blocks read access to OTP programming code from external viewing and disables the following commands: LDC , LDCI , LDE , and LDEI .
<u>Q</u> UIT	Quit OTP programming operations.

NOTES:

1. To obtain a Data I/O (such as UniSite, 3900, or 2900) calculated Checksum, the user should first download the file with 00 padding option, then perform the OTP programming. Data I/O fills its memory with 00 after power up.
2. Checksum differences may occur. During user-program download, the memory is padded with code outside the user program memory if the user program is less than the emulation memory. The memory is padded with either 00, FF, or nothing according to user selection. This does not effect the user program; however, it does effect the calculation of the Checksum during the OTP programming because the Checksum is calculated through out the whole memory size of the OTP.
3. RAM is Code Memory space.

Command Status

This field contains a message display area and a progress bar that is activated when one of the OTP operations are performed.

Device Serialization

A user-specified serial number can be programmed into the OTP device. The serial number can be changed at any time before it is programmed into a device. The characteristics of the serial number are specified in the Device Serialization section of the OTP window.

Method

Three methods of creating a serial number are available: sequential, pseudorandom, or none.

- Sequential Mode - The serial number increments by one after each device is programmed.
- Pseudorandom Mode - The serial number is generated according to the following algorithm:

$$r[i+1] = (a * r[i] + b) \bmod 2^N$$

where $r[i+1]$ is the new serial number

$r[i]$ is the last serial number

$$a = 1909$$

$$b = 221571$$

N = number of bits in the serial
number

Example: Entering 1000 in the Address box, selecting 4-Bytes as the Serial Number Size, and entering 12345678 in the Serial Number box outputs to the screen as follows:

$$\%1000 = \%12$$

$$\%1001 = \%34$$

$$\%1002 = \%56$$

$$\%1003 = \%78$$

- None

Serial Number Size

The Serial Number Size field specifies length in number of bytes the serial number occupies in memory. This field is not available when Device Serialization is set to None.

Serial Number

Enter the exact serial number in the input field. Specify the format of the number as either hexadecimal or decimal. If a decimal number is input, the number is converted to hexadecimal for storage in memory. This field is not available when Device Serialization is set to None.

Address

Enter a four-digit hexadecimal number to be the address where the serial number is to be stored in memory. This field is not available when Device Serialization is set to None.

CONFIGURATION

At start-up, the user selects the appropriate ICEBOX entry. When the Configuration dialog box is shown on the screen, you can choose the processor and the ROM size you want to emulate. The code file you download to Code Memory is limited to the ROM size selected. Selected ports automatically reference port availability in the selected processor. Other information about the processor, such as the number of ports and extended register banks, can be observed but cannot be altered. Click the OK or CANCEL button to confirm or cancel the configuration.

NOTE: To modify the configuration, you must first close all open windows; the Configuration command then reappears in the Main Menu.

When the Configuration menu item is selected, the Configuration dialog box is displayed.



Figure 3-35. Configuration Dialog Box

HELP

<u>F</u> ile	<u>V</u> iew	<u>Q</u> TP!	<u>C</u> onfiguration	<u>H</u> elp
				<u>I</u> ndex
				<u>K</u> eyboard
				<u>C</u> ommands
				<u>G</u> lossary
				<u>P</u> rocedure
				<u>U</u> sing Help
				<u>A</u> bout ICEBOX

The on-line Help program is available to provide brief help messages on various topics. The Help program features an index that easily references the emulator commands and procedures. Topics covered under Help include:

- Index of the Help Program
- Keyboard Commands

- ICEBOX GUI Commands
- Procedures for Using the Emulator
- How to Use Help
- Version and Copyright Information

WINDOW REFRESHING

When you make a change in the Code Memory window, the code view in the Debug window is updated automatically. When a program runs from the Debug window, the Status window is updated automatically at each step, Halt, or breakpoint. All other windows (Registers, Expanded Registers, Data Memory, and Code Memory) are not updated automatically as the program runs. To refresh the window display to reflect the current hardware values, double click the caption bar of each window.

EMULATOR OPERATION

Hardware Reset

When you press the emulator RESET button (or power-down, then power-up), the initial screen and the ZiLOG logo are shown while the ICEBOX goes through the initialization sequence.

Pressing the RESET button resets most of the settings that you establish using the GUI. For example, the emulated ROM size is reset to 4 KB, and all 64-KB breakpoint RAM is cleared.

NOTE: After RESET, you must wait a minimum of 5 seconds (for completion of selftests) before starting the GUI software.

Hardware Reset While GUI Is Running

If you press the Hardware Reset while the GUI is running, a communication error dialog box appears with the following message: Out of synchronization with the emulator. This message appears when the power or RS-232C cable is removed, or whenever the ICEBOX emulator and the host PC have failed to properly communicate with each other. You have three responses to this message: Abort, Retry, or Ignore.

Abort	Quits the GUI.
Retry	Resets the GUI by reinitializing. You are returned to the initial purple screen with the ZiLOG logo.
Ignore	Attempts to reestablish communication between the GUI and ICEBOX emulator without reinitializing.

ROM Size

The ICEBOX has the selectable ROM size of 4K to 64K.

Breakpoint Implementation

The emulator bases its breakpoint facility completely on addresses, rather than on inserting special TRAP instructions into the program, which enable the setting of breakpoints in Program Memory on your target board. The ICEBOX uses a 32Kx1 static RAM for each cycle emitted by the Z8 ICE chip during code execution to implement the breakpoint function in hardware.

EPROM/OTP Programmable Devices

Z89371

NOTE: Refer to the Data Sheet (DS) for exact OTP Programming requirements.

RUNNING MULTIPLE EMULATORS

The DSP ICE GUI allows several emulators to run at the same time, if your PC has more than one communication port.

NOTE: Running multiple emulations (two or more emulators running simultaneously) requires more memory than single-emulation operation; therefore, we recommend using a PC with 8-MB RAM or 16-MB RAM for Windows 95. In addition, two or more communication ports are needed when running multiple instances. Ensure that there are no COM port interrupt conflicts if installing additional COM ports via an add-on board.

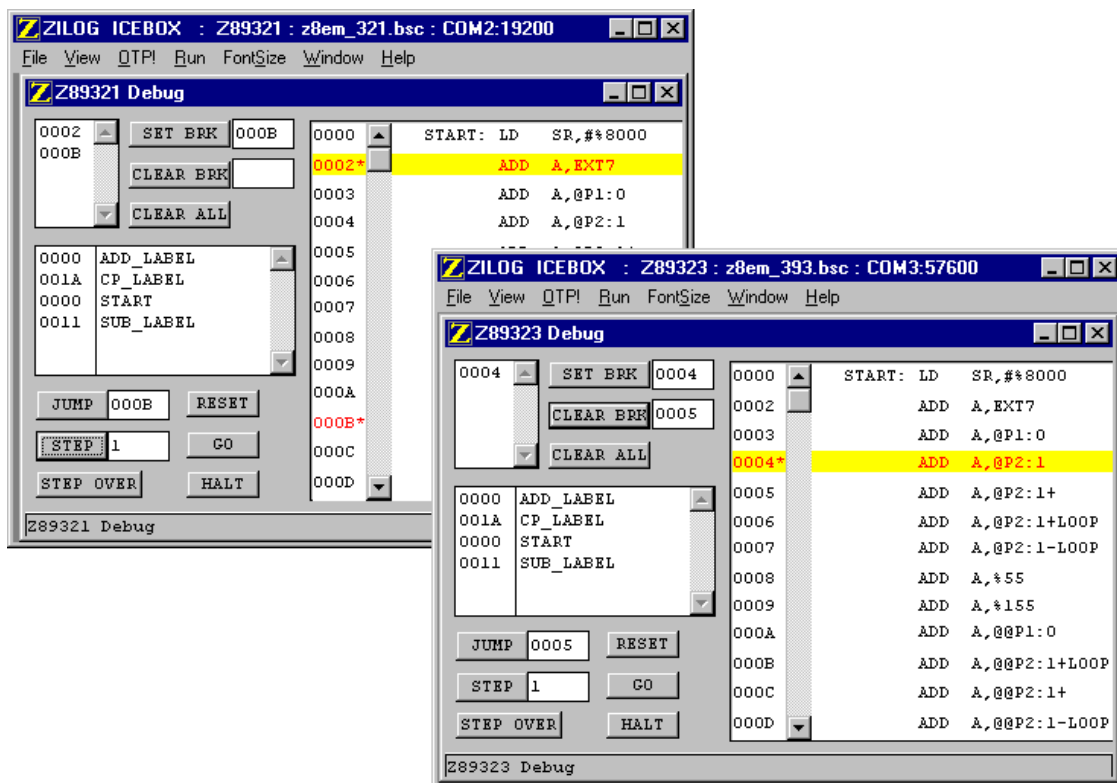


Figure 3-36. Multiple Emulator Operation Using 2 COM Ports

EVALUATION FUNCTIONS

The Z8932102ZEM combines the advantages of an in-circuit emulator and evaluator in one device. As an in-circuit emulator, it can trace user programs using an emulation pod to the target board. As an evaluator, it has extensive on-board periphery for quickly implementing and prototyping typical DSP systems.

The Z8932102ZEM has the following peripheral devices on board:

- 64K External Data Memory
- 8 LEDs for Indicating Program Status
- 8 Buttons for Data Input
- Analog Interface Consisting of:
 - 1 Input Connector (IN0) and 2 Output Connectors (Linear Output and Speaker)
 - Output Buffer Amplifier

These peripherals are mapped to the External device address map as follows:

Table 3-2. External Device Address Map

Designation	Function	Mode
EXT 0	SRAM Address	Write Only
EXT 1	SRAM Data	Read/Write
EXT 2	LEDs	Write Only
	Buttons	Read Only
EXT 3	Additional External Port	Read/Write
EXT 4-6	External Ports	Write Only

EXT3-6 do not access on-board peripherals. They are decoded by the Altera chip for accessing user peripherals.

PERIPHERAL INTERFACES

Initialization of Peripherals

The periphery is shown on the schematic in Appendix B.

The following recommended instructions appropriately initialize the peripherals:

```
ld      ext7,#15

ld      ext3,#68h          ; 0000 0000 0110 1000

                                ; 1 wait for:

                                ; reading SRAM

                                ; reading buttons

ld      ext7,#4

ld      a,#0

ld      ext2,a              ; switch OFF all the LEDs
```

Review any additional external ports used regarding the need for wait states.

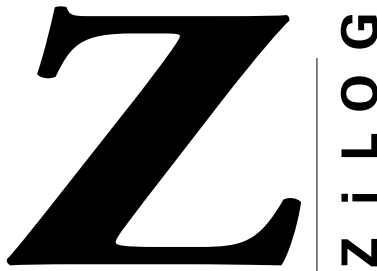
External Data Memory

The external ports used for Data Memory include:

Table 3-3. External Data Memory External Ports

Designation	Function	Mode
EXT 0	SRAM Address	Write Only
EXT 1	SRAM Data	Read/Write

This memory is primarily for array storage. Write the desired first address, then write or read data as many times as needed. Addresses increment automatically after each access to memory. This feature speeds up access to the array in most applications. If you want to read and write to the same address, you must write the address before each access.



CHAPTER 4

Totally Logical

SAMPLE SESSION

DSP EMULATOR SAMPLE SESSION

This sample session is designed to introduce you to the features of the DSP graphical user interface (GUI) software and thoroughly acquaint you with the various GUI windows you will be using in this program. For best results, follow the steps in sequential order. Topic headings, such as “Using the Debug Window”, have been added so you can better locate how-to information.

Additional information is available in Chapter 3, “Summary of Menus, Commands, and Operations” and in Chapter 5, “Troubleshooting Guide”.

NOTES:

1. In order to properly demonstrate the program features, be prepared to import sample files in this session. A sample DSP source code file and sample assembly batch files have been included on the ZMASM Diskette; however, other sample files of your choice can be used. You can also download other sample files from ZiLOG's Bulletin Board Service (ZBBS) or Internet site. (Refer to Appendix A for information.)
2. Consult ZiLOG's ZBBS or www.zilog.com to obtain the latest released version of DSP GUI software.
3. This sample session assumes the ZiLOG Macro Cross Assembler has been installed.
4. The screen images contained in this file are representative only. Your screen display may vary.

STARTING THE APPLICATION

1. Press the RESET button on the emulator after power up.
2. Double-click the DSPICE program icon. The ZiLOG ICEBOX Connect dialog box is displayed.

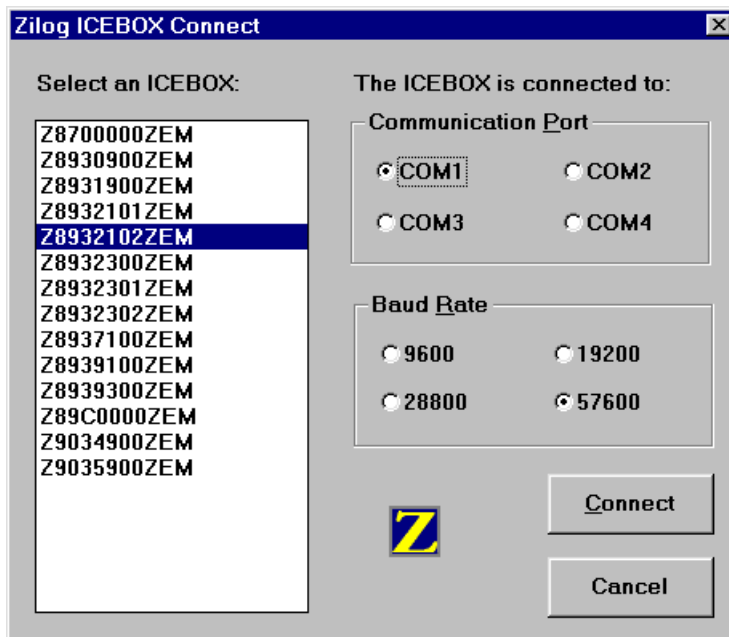


Figure 4-1. ZiLOG ICEBOX Connect Dialog Box

3. Select the Z8932102ZEM ICEBOX, the correct COM port, and the appropriate baud rate for the communication to the PC; then click on the **Connect** button and wait for the initialization procedure to complete. The Configuration dialog box is displayed.

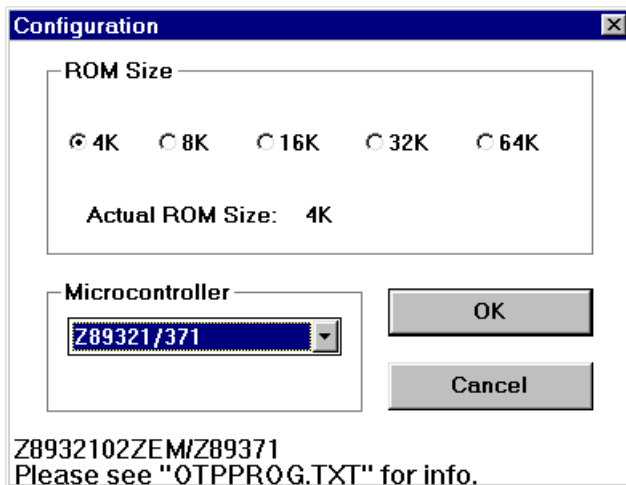


Figure 4-2. Configuration Dialog Box

Changes to the emulator, COM port, and baud rate are saved and become the new default the next time the icon is activated. If the communication attempt between the PC and the emulator fails, check the connection or reset the emulator. (Refer to Chapter 5: “Troubleshooting Guide”.)

DSP MICROCONTROLLER EMULATION

4. In the MicroController field (see Figure 4-2), select Z89321/371; then click on the 64K ROM Size option button, if it is not already selected (see Figure Figure 4-3); and click on the OK button (or press the ENTER key) to accept the configuration. The Main Menu is displayed.

<u>F</u>ile	<u>V</u>iew	<u>O</u>TP!	<u>C</u>onfiguration	<u>H</u>elp
--------------------	--------------------	--------------------	-----------------------------	--------------------

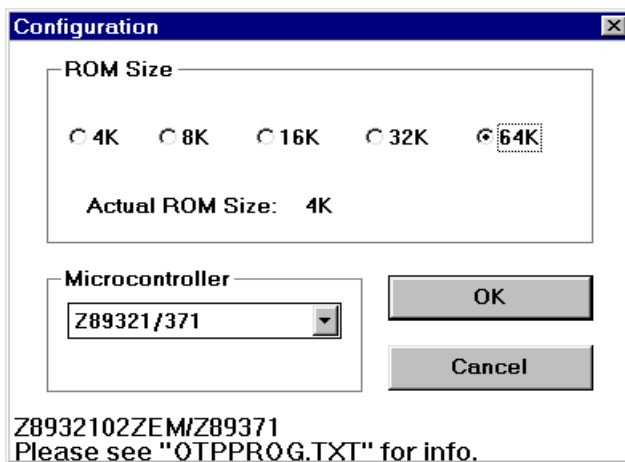


Figure 4-3. Configuration Dialog Box With 64K ROM Size Selected

Emulation ROM Size selection affects the size of the code file you can download.

The Configuration dialog box is only accessible when no other window is open.

NOTE: Refer to the Product Information Sheet for a complete list of precautions and limitations.

5. Click on File, then Download Application. The Download Application window is displayed.

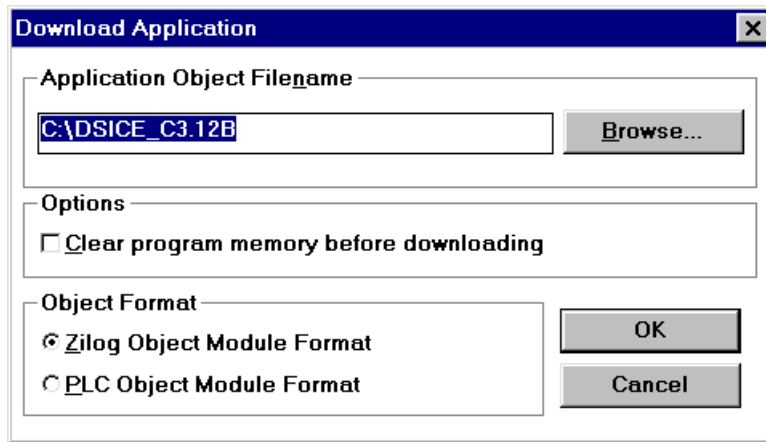


Figure 4-4. Download Application Dialog Box

6. Click on Browse. The Load File Browse dialog box is displayed.

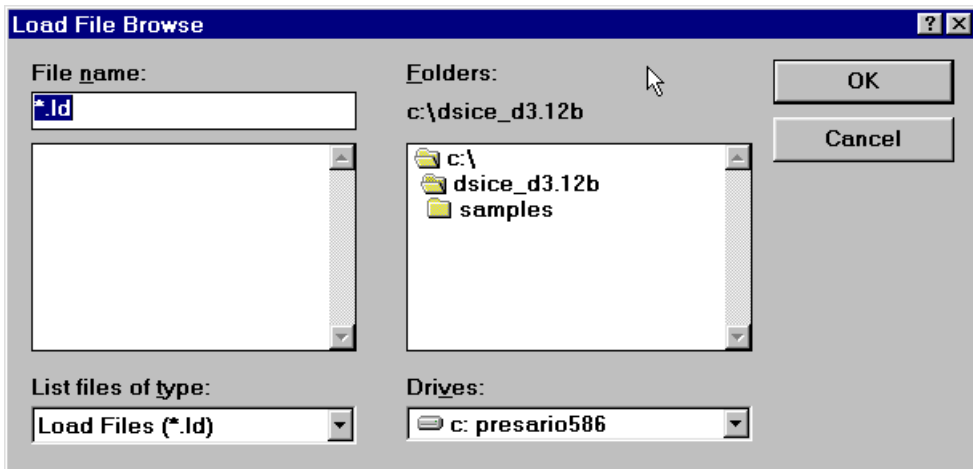


Figure 4-5. Load File Browse Dialog Box

7. Change the current path to the ZiLOG Developer Studio, for example, c:\progra~1\zds_j2.00. The names of two subfolders are displayed.
8. Double click on the samples entry. Additional names of subfolders are displayed in the same area of the window.

9. Double click on the entry 89c00. An entry, suite.ld, is displayed to the area on the left side of the window.

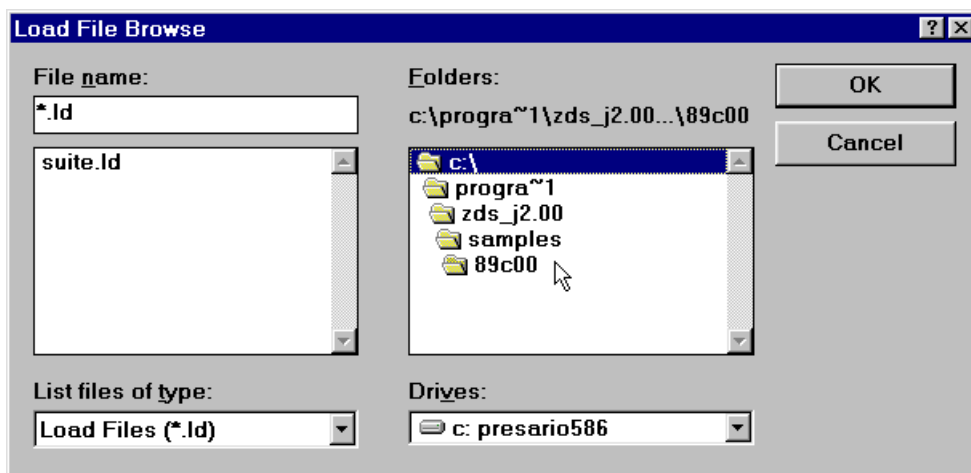


Figure 4-6. Load File Browse Dialog Box With SUITE.LD File Selection

10. Double click on the entry suite.ld. The Download Application dialog box is displayed.

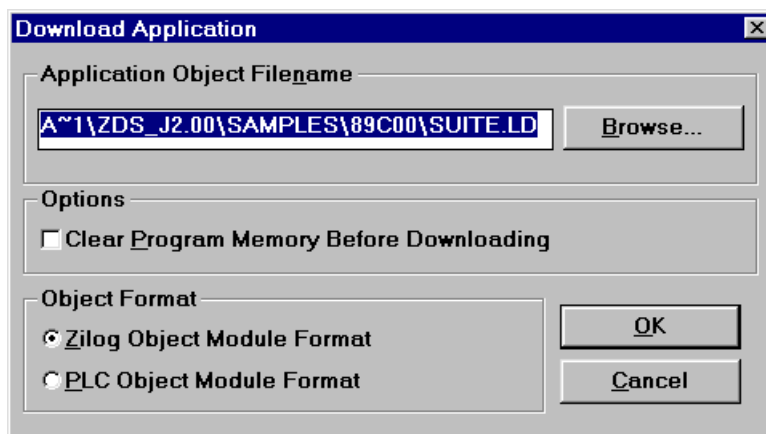


Figure 4-7. Download Application Dialog Box With SUITE.LD Selection

- Click on the option Clear Program Memory Before Downloading, then click on OK. The contents of file suite.ld is loaded into memory, and the Main Menu is displayed.

File	View	QTP!	Configuration	Help
------	------	------	---------------	------

Monitoring/Modifying Registers

- Click on View, then Registers. The Z89321 Registers dialog box is displayed.

Z89321 Registers

Internal Registers		RAM Pointers		Flags	
A	000001	P	50B3F6	P0:0	FF
X	80FF	Y	AEAA	P0:1	FF
PC	FFB9	SR	0000	P1:0	FF
				P1:1	FF
				P2:0	FF
				P2:1	FF

Flags: ☐ N ☐ OV ☐ Z ☐ C
☐ SH3 ☐ OP ☐ IE
 Loop Size = 256

External Registers		Data Pointers		Stack	
EXT0	FFFF	EXT4	8000	D0:0(%0)	0001
EXT1	FFFF	EXT5_1	0000	D0:1(%0)	0000
EXT2	FFFF	EXT5_2	FFFF	D1:0(%1)	0010
EXT3	FFFF	EXT6_1	0000	D1:1(%1)	0000
		EXT6_2	FFFF	D2:0(%2)	0400
		EXT7_1	0000	D2:1(%2)	1010
		EXT7_2	0000	D3:0(%3)	8002
				D3:1(%3)	0008

Stack: 0 0000, -1 FFFF, -2 FFFF, -3 FDFD, -4 DFFF, -5 DFFF

Figure 4-8. Z89321 Registers Dialog Box

If the characters are either too small or too large, use the Font Size menu to select an appropriate font size.

NOTES:

- Be careful when modifying ports or read-only registers because the values of these registers are not editable. That is, they cannot be changed using this screen.
- The values displayed for write-only registers may not represent the actual values.

When the Z89321 Registers window is displayed, the following menu is displayed:

File	View	QTP!	Font Size	Window	Help
------	------	------	-----------	--------	------

13. Click on View, then RAM Bank0. The Z89321 RAM Bank0 window is displayed.

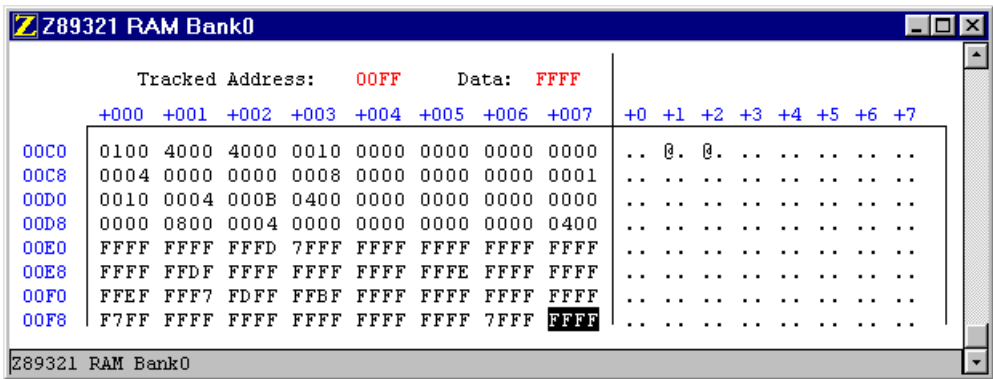


Figure 4-9. Z89321 RAM Bank0 Window

The following menu bar is displayed:

<u>F</u> ile	<u>V</u> iew	<u>O</u> TP!	<u>E</u> dit	<u>T</u> racking	<u>F</u> ont <u>S</u> ize	<u>W</u> indow	<u>H</u> elp
--------------	--------------	--------------	--------------	------------------	---------------------------	----------------	--------------

14. Click on Edit, then Fill. The Memory Fill dialog box is displayed.

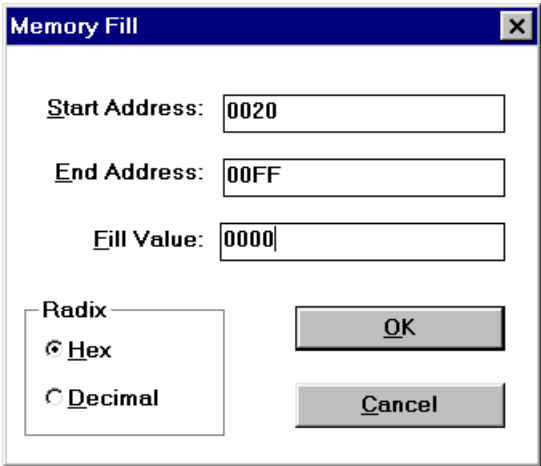


Figure 4-10. Memory Fill Dialog Box

15. Type 0020 in the Start Address field, then 0000 in the Fill Value field, and click on OK. The Z89321 RAM Bank0 window updates to reflect the new memory contents.

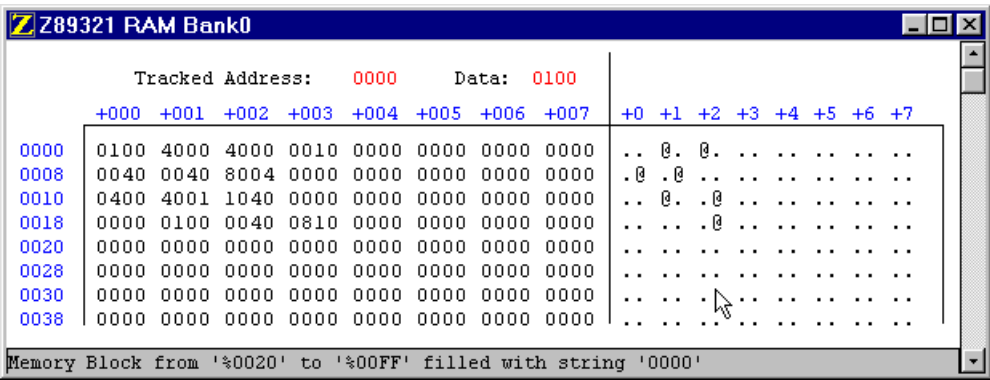


Figure 4-11. Z89321 RAM Bank0 Dialog Box With Memory Block 0020 - 00FF Zeroed

16. Click on the content of RAM Bank0, then right-click using your two-button mouse. The following items are made available:

- Clear All
- Fill...
- Tracking Absolute Address
- Download DSP RAM0...
- Upload DSP RAM0...

Selection of any of these items results in the display of the corresponding Edit or Tracking menu items.

17. Select Clear All, then With 0000. The Clear All Memory window is displayed.

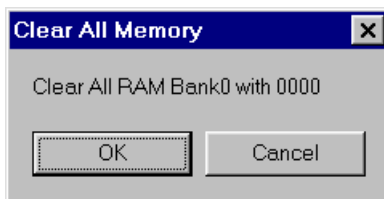


Figure 4-12. Clear All Memory Window

18. Click on OK. The Z89321 RAM Bank0 window updates to reflect the new memory contents.

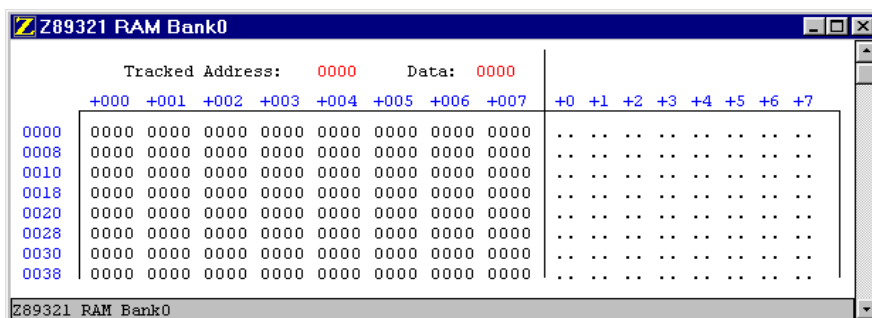


Figure 4-13. Z89321 RAM Bank0 Dialog Box With Zeroed Registers

19. Close the Z89321 RAM Bank0 dialog box. The Z89321 Registers window remains displayed. The following menu bar is displayed.

File	View	QTP!	Font Size	Window	Help
-------------	-------------	-------------	------------------	---------------	-------------

20. Click on View, then DSP RAM Bank1. The Z89321 RAM Bank1 window is displayed.

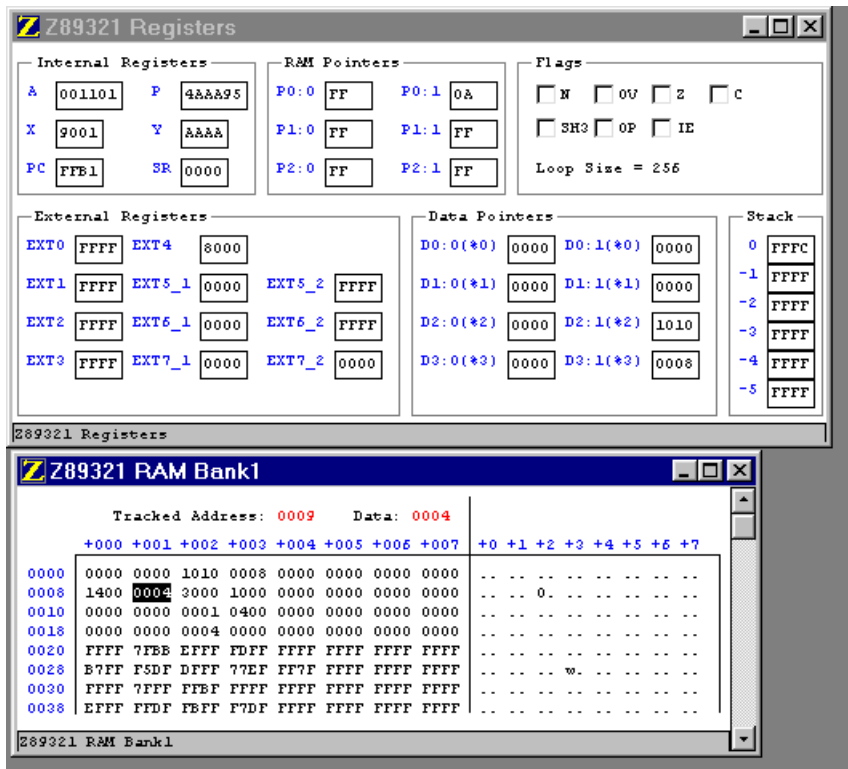


Figure 4-14. Z89321 Registers and Z89321 RAM Bank1 Windows

The following menu bar is displayed:

File	View	QTP!	Edit	Tracking	Font Size	Window	Help
-------------	-------------	-------------	-------------	-----------------	------------------	---------------	-------------

21. Type 00AD into the memory location highlighted, which is location row 0008 column 001.

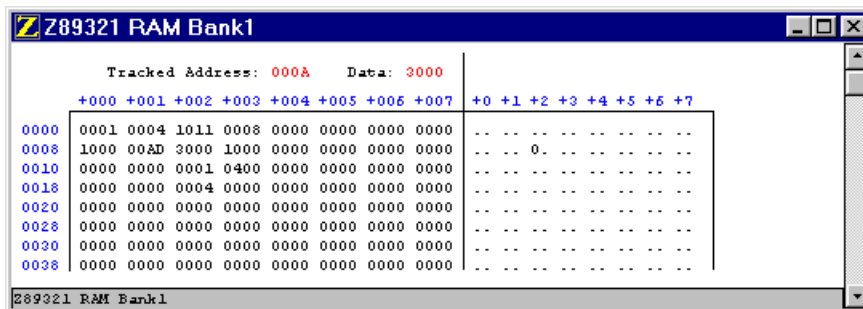


Figure 4-15. RAM Bank1 With Value 00ADH At Location 09H

22. In the P0:1 field of the RAM Pointer section of the Z89321 Registers window, type 9.

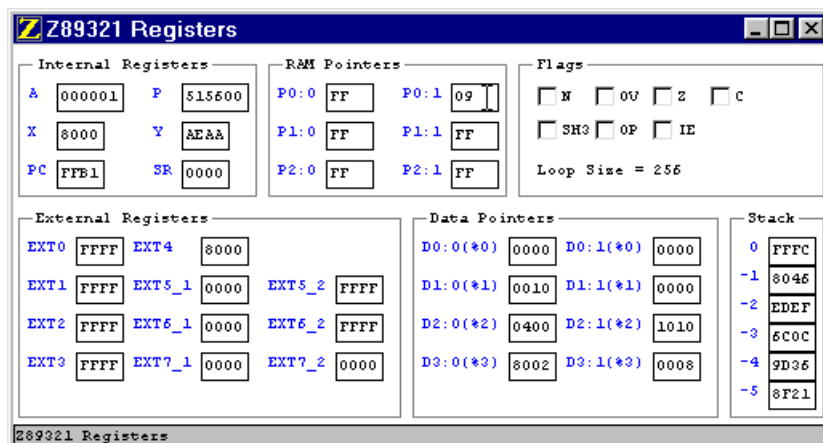


Figure 4-16. Set Pointer P0:1

23. Click on the Z89321 RAM Bank1 window, and Tracking, and Pointer Register P0:1. The cursor highlights the memory location at row 0008 column 001, and the following message is displayed at the bottom of the window:
Tracking @P0:1, memory address = %9

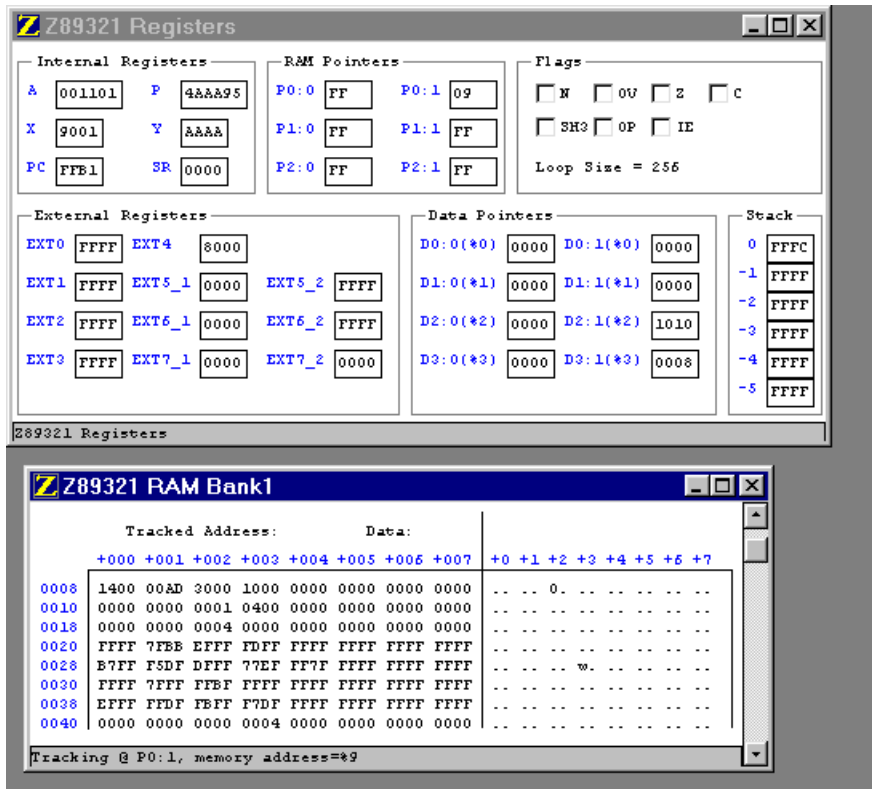


Figure 4-17. Tracking Pointer P0:1 in RAM Bank1

24. Click on View, then DSP Code Memory. The Z89321 DSP Memory window is displayed in addition to the Z89321 Registers and Z89321 RAM Bank1 windows.

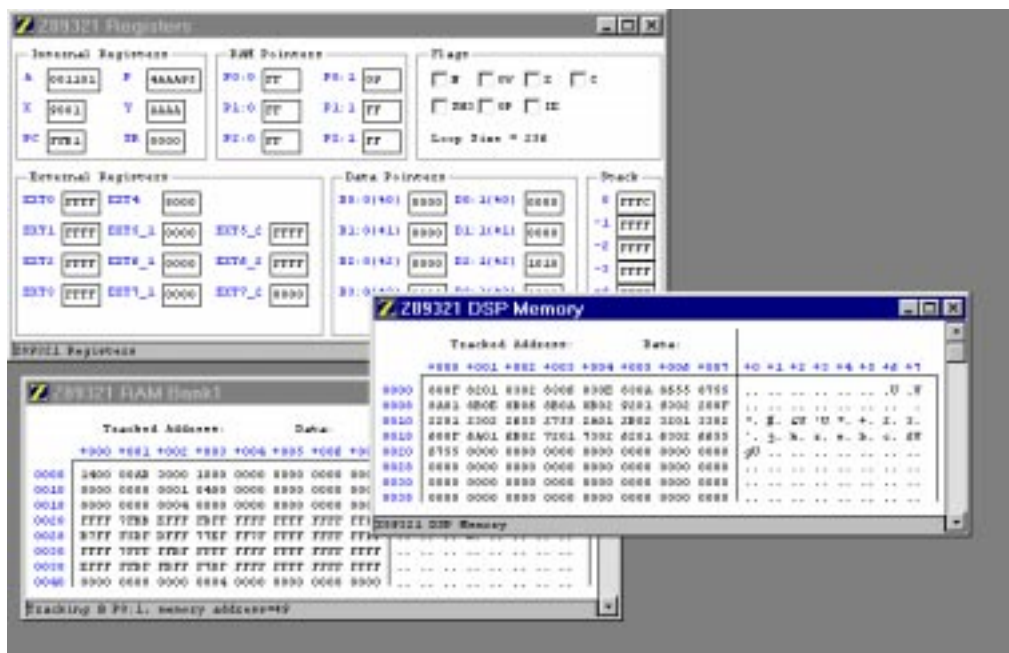


Figure 4-18. DSP Code Memory, Z89321 Registers, and Z89321 RAM Bank1 Windows

25. Click on Tracking, then @@P0:1. The value 0000, the contents of row 00A8 column 0005, is high-lighted.

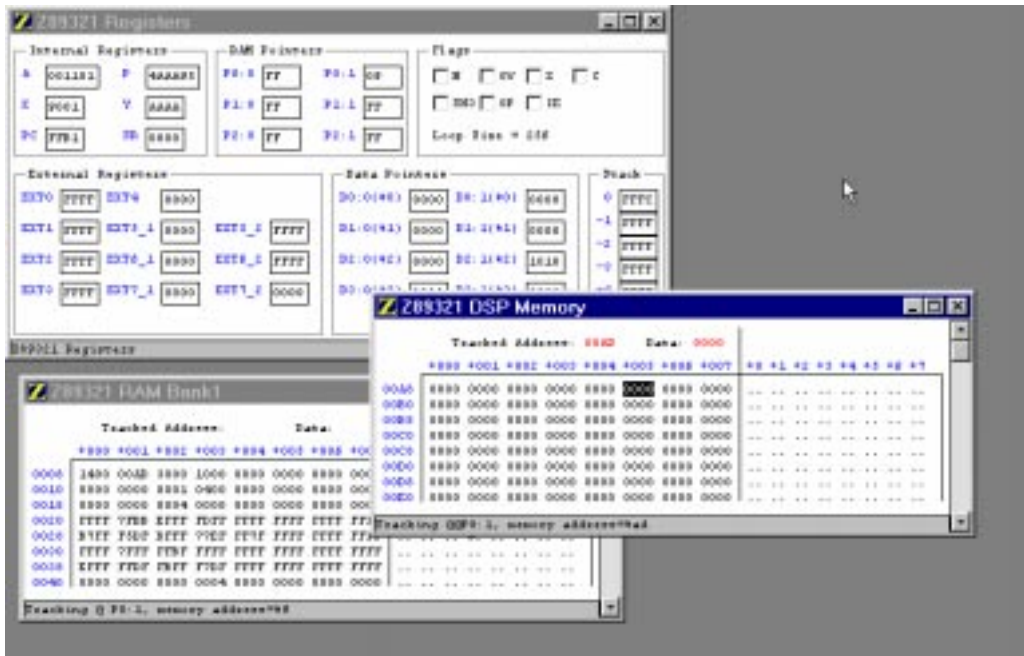


Figure 4-19. The Z89321 Registers, RAM Bank1, and Z89321 DSP Memory Windows

26. Close the Z89321 DSP Memory, Z89321 RAM Bank1, and Z89321 Registers windows by clicking on the X button in the upper right-hand corner of each window.

Tracing Code Using DSP Debug

When downloading a hexadecimal file, the file size showing in the file downloading dialog box is the size of the memory the downloaded file will occupy, not the actual file size.

27. Click on View, then Debug.

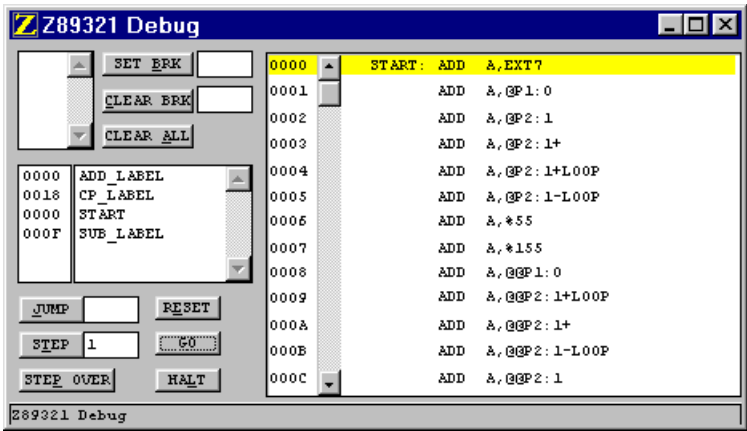


Figure 4-20. Z89321 Debug Dialog Box

Notice that the following menu is displayed when the Z89321 Debug dialog box is displayed:

File	View	QTP!	Run	FontSize	Window	Help
------	------	------	-----	----------	--------	------

28. Click on Run. The following menu is displayed:

File	View	QTP!	Run	FontSize	Window	Help
			Trace Code			
			Trace Call			
			Animate			
			Clear Trace			
			Log Execution			
			Log Options...			

29. Click on Log Options. The Debug Log dialog box is displayed.

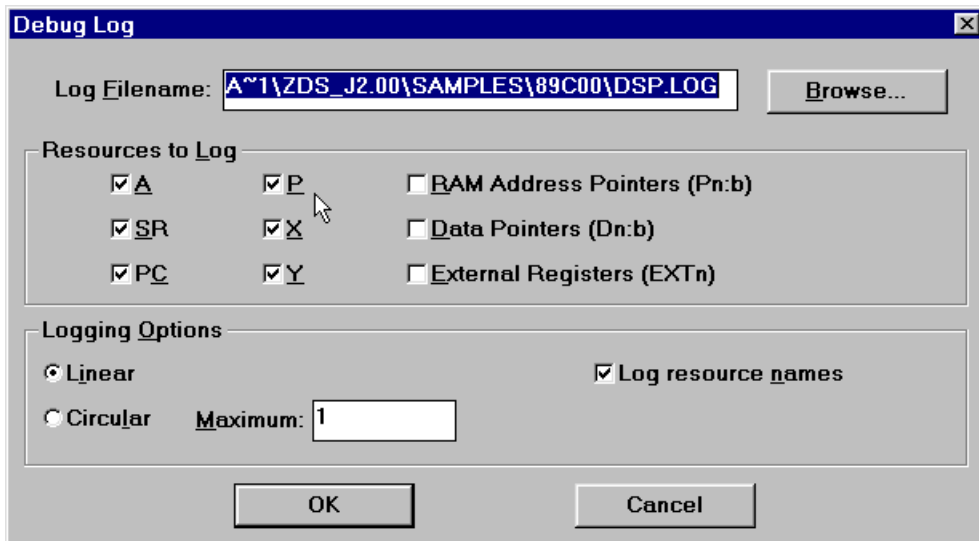


Figure 4-21. Debug Log Dialog Box

The default path and name for a log file are displayed in the Log Filename field. To change the name or path, type a new entry or use the Browse button. Resources can be selected or deselected. Logging Options can be selected for configuring your trace session.

30. Click on OK. The Z89321 Debug dialog box is displayed (see Figure 4-20).
31. Click on Run, then Trace Code. The menu closes.
32. Click on Run. Notice that the Trace Code and Animate items are checked.
33. Click on Run again to close the menu.

34. Place your cursor in the JUMP field, then type 0008.

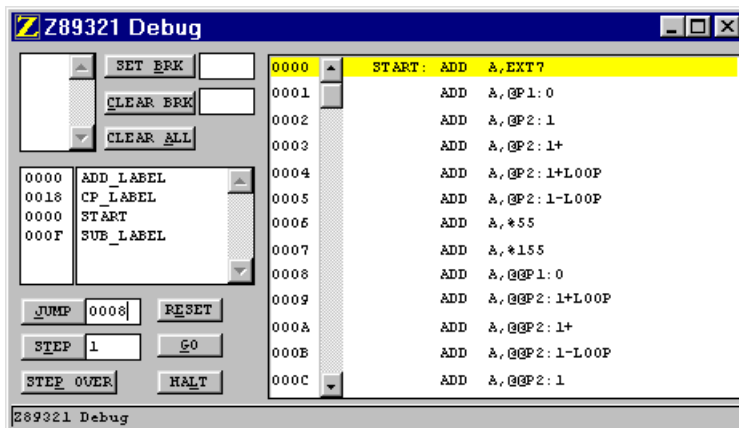


Figure 4-22. Z89321 Debug Window With Jump Input

35. Click on the JUMP button. The current line in the code display section moves to address 0008h.

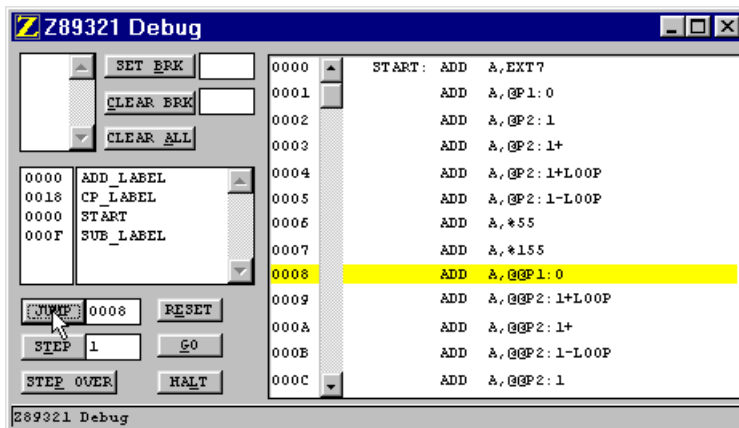


Figure 4-23. Z89321 Debug Window After Jump

36. Click on RESET to return the current line to the top of the code.

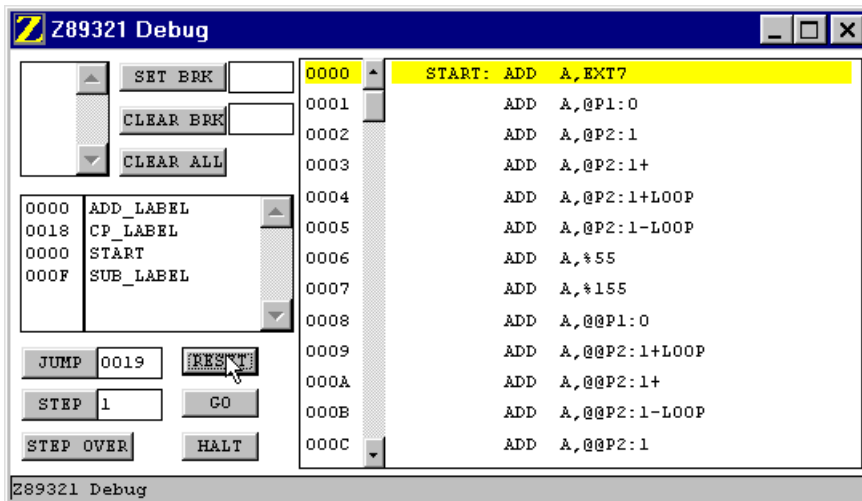


Figure 4-24. Z89321 Debug Window After RESET

37. Click on Halt to stop execution.

38. Double click on the 000Fh in the Label section of the window to put the 000Fh address in the SET BRK field, then click on SET BRK. A breakpoint is set at address 000Fh.

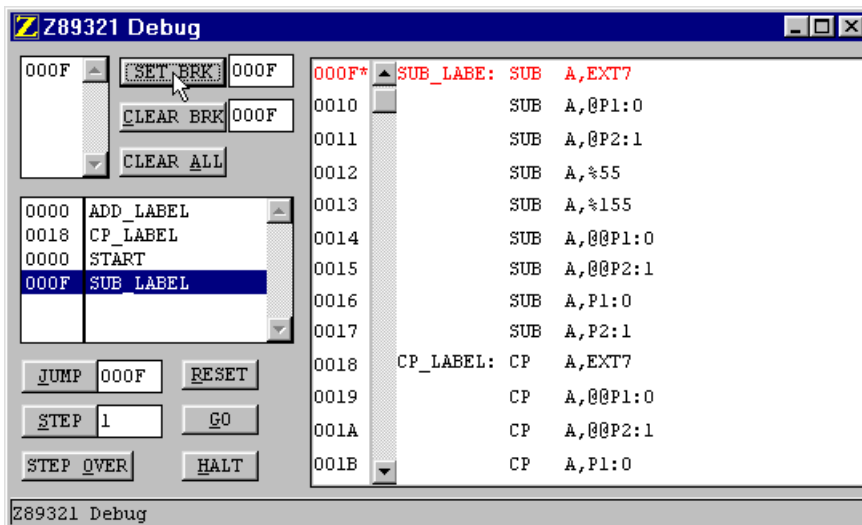


Figure 4-25. Z89321 Debug Window With First Breakpoint Set

39. Click on Run, then Log Execution to enable capturing the data log file that was specified in the path C:\DSPICE_C3.12B\DSP.log.
40. Click on GO to resume execution.

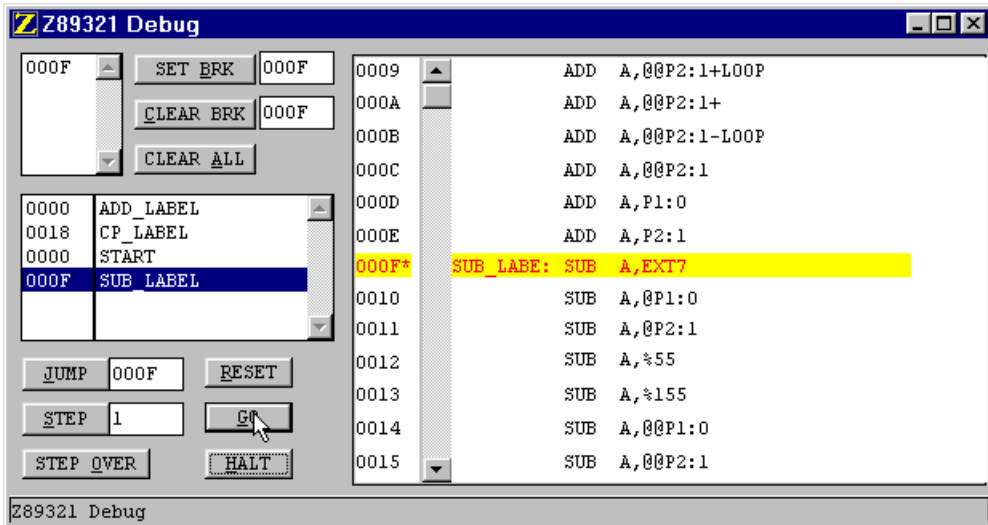


Figure 4-26. Z89321 Debug Window At Breakpoint After GO

NOTES:

1. The mouse cursor is changed to an hourglass shape. The only action allowed after a GO is HALT. If no breakpoints are set, click on the HALT button to stop execution.
2. If the application inadvertently enters STOP mode, the way to halt the emulator execution is by doing a Stop-Mode Recovery (as defined by the user program). You may also reset the application using the RESET button on the emulator.

41. Place your cursor in the SET BRK field, then change the value to 0018, and click on SET BRK. A second breakpoint is set at address 0018h.

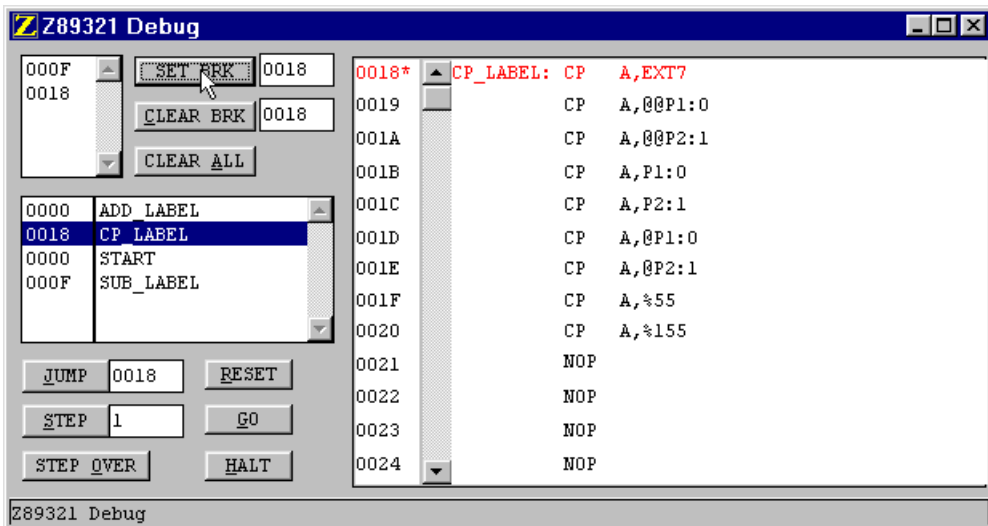


Figure 4-27. Z89321 Debug Window With Two Breakpoints

42. Click on RESET to return to the beginning of the code, then click on GO to begin execution. The program stops at address 000Fh, the first breakpoint.

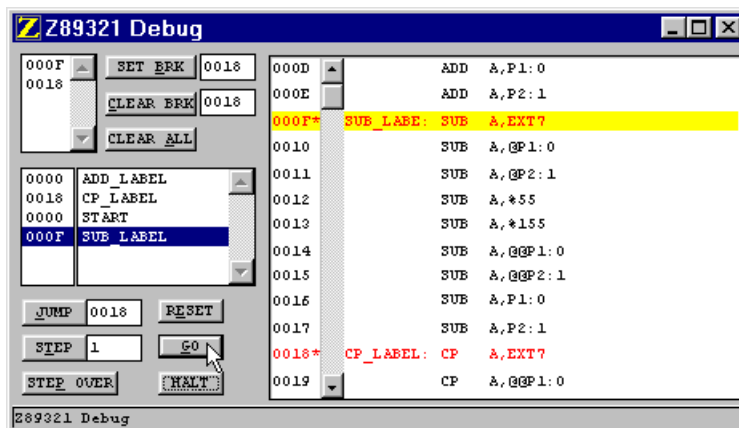


Figure 4-28. Z89321 Debug Window After GO

43. Click on CLEAR BRK to remove the breakpoint at line 0018h. The breakpoint list window shows one breakpoint at line 000F.

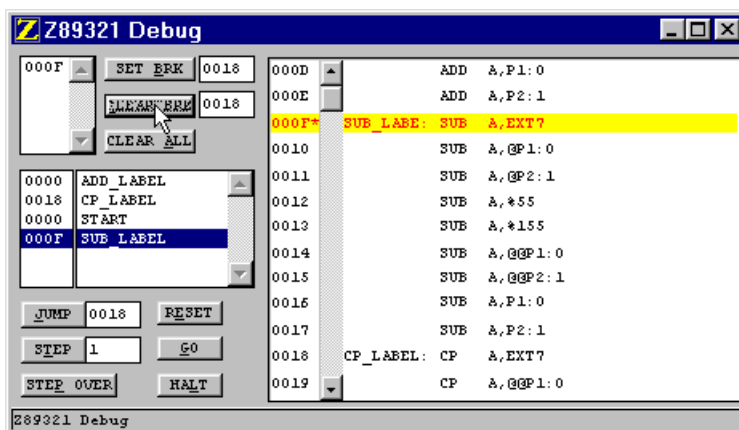


Figure 4-29. Z89321 Debug Window With Second Breakpoint Cleared

44. Click on RESET to return to the beginning of the code, then put your cursor in the CLEAR BRK field, and type 000F, and click on CLEAR BRK to remove the original breakpoint.

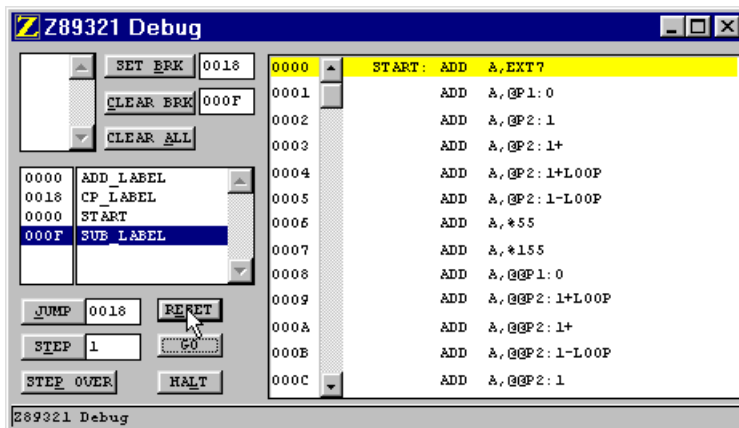


Figure 4-30. Z89321 Debug Window With Original Breakpoint Cleared

45. Click on GO to resume execution, then HALT to stop execution. Execution is halted but tracing continues.

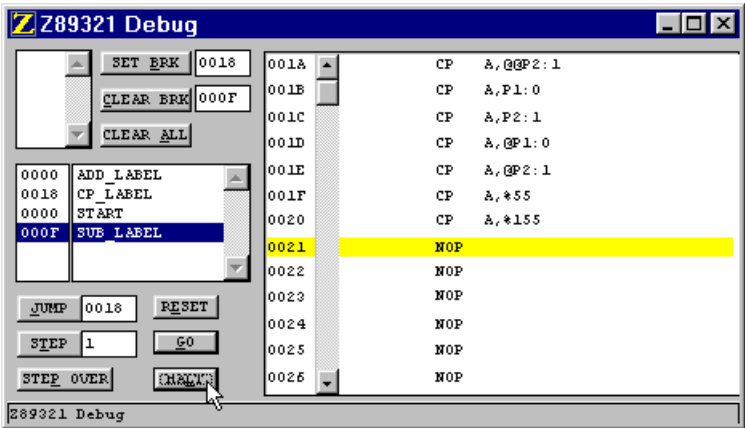


Figure 4-31. Z89321 Debug Window After HALT

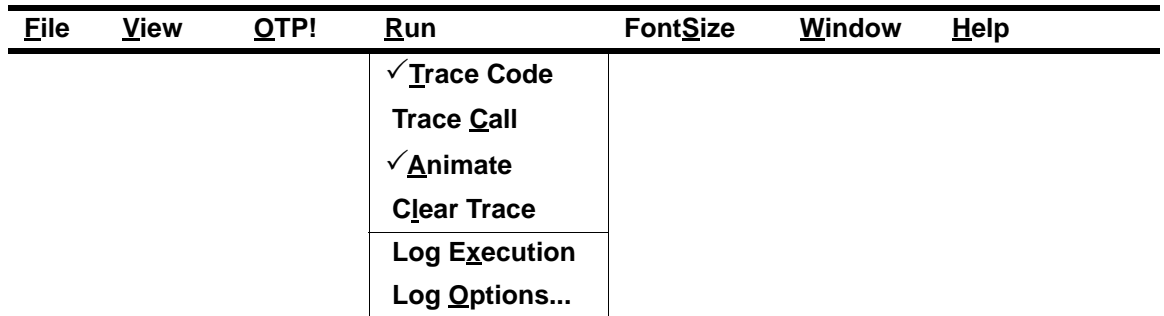
46. Click on Run, then Clear Trace to end tracing.

File	View	QTP!	Run	FontSize	Window	Help
			<div><div>✓Trace Code</div><div>Trace Call</div><div>✓Animate</div><div>Clear Trace</div></div>			
			<div><div>✓Log Execution</div><div>Log Options...</div></div>			

The menu closes.

File	View	QTP!	Run	FontSize	Window	Help
------	------	------	-----	----------	--------	------

47. Click on Run, then Log Execution. Log Execution is deselected and capturing of the data into the log is stopped.



48. Use any text editor, like WordPad or Word, then open your log file at the path C:\DSPICE_C3.12B\DSP.log to view the trace log.

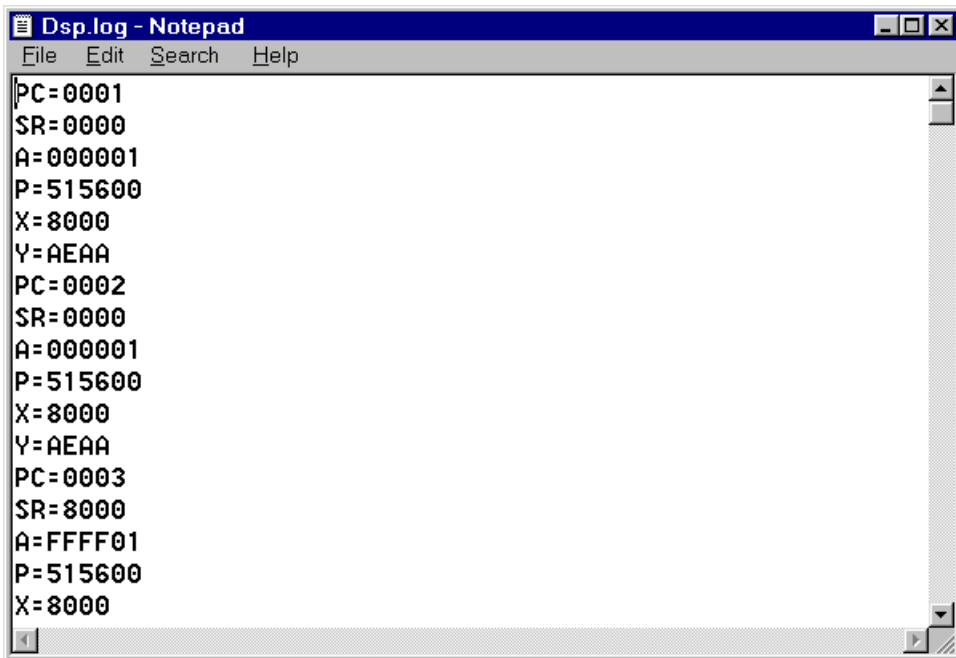


Figure 4-32. DSP Log File Sample

Window Refresh

49. Double-click on the caption bar of the DSP Registers dialog box to refresh it and check the value of the registers. You may also use the Window menu to activate a new window.

Save and Quit

50. Click on File, then Save Session to save the current setting in a session file.

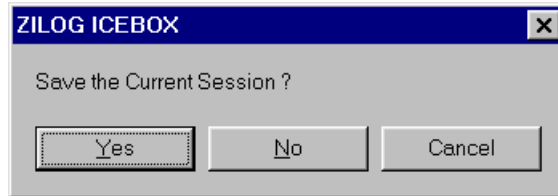


Figure 4-33. Save Session Dialog Box

51. You can reload the last session by using the Open Session command on the File menu.

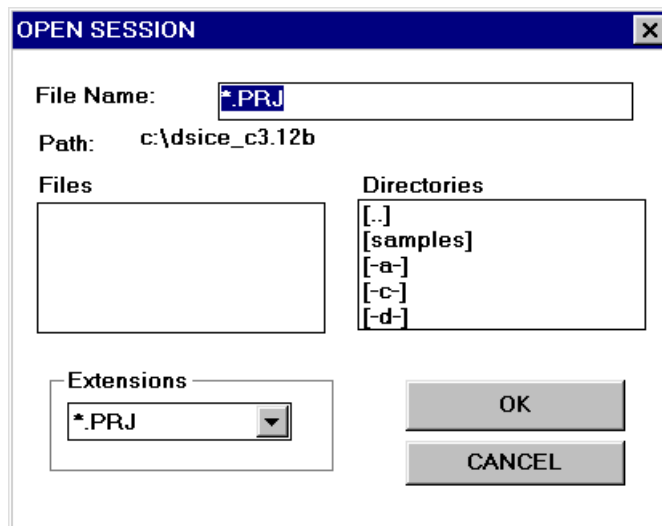


Figure 4-34. Open Session Dialog Box

52. Quit the application by clicking on File, then Exit (or ALT+F4) to end this sample session.

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CHAPTER 5

TROUBLESHOOTING GUIDE

INTRODUCTION

Before contacting your ZiLOG representative or submitting a Problem Report, please follow these simple steps. Also, check the Precautions and Limitations sections in the Product Information document included with your emulator to eliminate other possible known problems. If a hardware failure is suspected, contact your local ZiLOG representative for assistance.

INITIAL SCREEN DOES NOT APPEAR

The initial ZiLOG screen does not appear after selecting a COM port, and a message is displayed that begins "Time-out while reading..."

1. Check the RS-232 cable connection or try another cable that is known to be good.
2. Check if transmit/receive signals need to be swapped.

NOTE: On some DB9 connectors for the COM ports, the transmit/receive signal may be swapped and a Null Modem adapter may be required.

3. Verify that the power supply is connected and turned On, and that power is available.
4. Verify that the power supply is set at the correct voltage.
5. Check that the power supply can supply the required 1.2 Amp current to the emulator.
6. Ensure that the correct crystal oscillator is installed.
7. Check selected port using another application or select another COM port.

XTAL OSCILLATOR CONTENTION.

Ensure that J9 and J10 are set to the appropriate position.

“CAN'T OPEN WINDOWS” MESSAGE

If the “Can't Open Windows” message appears while attempting to open a window using the GUI software, there may not be enough memory within the Microsoft Windows environment to properly run the GUI software. Try closing the other active applications or exit and reenter the Microsoft Windows environment.

“OUT OF SYNCHRONIZATION WITH THE EMULATOR” MESSAGE

This message appears whenever communication between the emulator and the PC is interrupted.

1. Ensure that the power cable is connected.
2. Ensure that the RS-232 cable is connected.
3. Change the baud-rate setting. the default value is 19200. A lower setting usually improves communications reliability.
4. The Emulator hardware RESET button was pressed while the GUI was running.
There are three responses to the message:

Abort Quits the GUI altogether.

Retry Resets the GUI by reinitializing. You are returned to the initial screen with the ZiLOG logo.

Ignore Attempts to reestablish communication between the GUI and emulator without reinitializing.



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APPENDIX A

ACCESSING THE ZBBS/INTERNET

BULLETIN BOARD INFORMATION

The ZiLOG Bulletin Board Service (ZBBS) currently provides basic information on ZiLOG products and includes a ROM CODE upload area. In addition, the ZBBS provides valuable information on items of interest, such as ZiLOG specialty software and documentation.

How to Access the ZBBS

The ZBBS can be reached by dialing 1-408-558-8890. The ZBBS supports speeds up to 28.8K Baud with connections 8-N-1 (8 bits, No parity, 1 stop bit). We recommend that you use an ANSI/BBS terminal emulation setup.

To preview information or download files, follow the on-screen instructions.

The latest production released version of the DSP GUI software can be downloaded from this site.

ZILOG ON THE INTERNET

ZiLOG has a Home Page on the Internet. The Home Page address is:

<http://www.zilog.com>

The ZiLOG Home Page includes valuable information about hardware and software development tools. The latest production released version of the DSP GUI software can be downloaded from this site.

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APPENDIX B

EMULATOR SCHEMATIC

The next page contains the schematic for the Z8932101ZEM board.

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This is a dummy page for the Z8932102ZEM schematic.



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APPENDIX C

ASCII CHARACTER SET

ASCII CHARACTER SET

Table 5-1. ASCII Character Set

Graphic	Decimal	Hexidecimal	Comments
	0	0	Null
	1	1	Start Of Heading
	2	2	Start Of Text
	3	3	End Of Text
	4	4	End Or Transmission
	5	5	Enquiry
	6	6	Acknowledge
	7	7	Bell
	8	8	Backspace
	9	9	Horizontal Tabulation
	10	A	Line Feed
	11	B	Vertical Tabulation
	12	C	Form Feed
	13	D	Carriage Return
	14	E	Shift Out
	15	F	Shift In

Table 5-1. ASCII Character Set (Continued)

Graphic	Decimal	Hexidecimal	Comments
	16	10	Data Link Escape
	17	11	Device Control 1
	18	12	Device Control 2
	19	13	Device Control 3
	20	14	Device Control 4
	21	15	Negative Acknowledge
	22	16	Synchronous Idle
	23	17	End Of Block
	24	18	Cancel
	25	19	End Of Medium
	26	1A	Substitute
	27	1B	Escape
	28	1C	File Separator
	29	1D	Group Separator
	30	1E	Record Separator
	31	1F	Unit Separator
	32	20	Space
!	33	21	Exclamation Point
"	34	22	Quotation Mark
#	35	23	Number Sign
\$	36	24	Dollar Sign
%	37	25	Percent Sign
&	38	26	Ampersand
'	39	27	Apostrophe

Table 5-1. ASCII Character Set (Continued)

Graphic	Decimal	Hexidecimal	Comments
(40	28	Opening (Left) Parenthesis
)	41	29	Closing (Right) Parenthesis
*	42	2A	Asterisk
+	43	2B	Plus
,	44	2C	Comma
-	45	2D	Hyphen (Minus)
.	46	2E	Period
/	47	2F	Slant
0	48	30	Zero
1	49	31	One
2	50	32	Two
3	51	33	Three
4	52	34	Four
5	53	35	Five
6	54	36	Six
7	55	37	Seven
8	56	38	Eight
9	57	39	Nine
:	58	3A	Colon
;	59	3B	Semicolon
<	60	3C	Less Than
=	61	3D	Equals
>	62	3E	Greater Than
?	63	3F	Question Mark

Table 5-1. ASCII Character Set (Continued)

Graphic	Decimal	Hexidecimal	Comments
@	64	40	Commercial At
A	65	41	Uppercase A
B	66	42	Uppercase B
C	67	43	Uppercase C
D	68	44	Uppercase D
E	69	45	Uppercase E
F	70	46	Uppercase F
G	71	47	Uppercase G
H	72	48	Uppercase H
I	73	49	Uppercase I
J	74	4A	Uppercase J
K	75	4B	Uppercase K
L	76	4C	Uppercase L
M	77	4D	Uppercase M
N	78	4E	Uppercase N
O	79	4F	Uppercase O
P	80	50	Uppercase P
Q	81	51	Uppercase Q
R	82	52	Uppercase R
S	83	53	Uppercase S
T	84	54	Uppercase T
U	85	55	Uppercase U
V	86	56	Uppercase V
W	87	57	Uppercase W

Table 5-1. ASCII Character Set (Continued)

Graphic	Decimal	Hexidecimal	Comments
X	88	58	Uppercase X
Y	89	59	Uppercase Y
Z	90	5A	Uppercase Z
[91	5B	Opening (Left) Bracket
\	92	5C	Reverse Slant
]	93	5D	Closing (Right) Bracket
^	94	5E	Circumflex
_	95	5F	Underscore
`	96	60	Grave Accent
a	97	61	Lowercase A
b	98	62	Lowercase B
c	99	63	Lowercase C
d	100	64	Lowercase D
e	101	65	Lowercase E
f	102	66	Lowercase F
g	103	67	Lowercase G
h	104	68	Lowercase H
i	105	69	Lowercase I
j	106	6A	Lowercase J
k	107	6B	Lowercase K
l	108	6C	Lowercase L
m	109	6D	Lowercase M
n	110	6E	Lowercase N
o	111	6F	Lowercase O

Table 5-1. ASCII Character Set (Continued)

Graphic	Decimal	Hexidecimal	Comments
p	112	70	Lowercase P
q	113	71	Lowercase Q
r	114	72	Lowercase R
s	115	73	Lowercase S
t	116	74	Lowercase T
u	117	75	Lowercase U
v	118	76	Lowercase V
w	119	77	Lowercase W
x	120	78	Lowercase X
y	121	79	Lowercase Y
z	122	7A	Lowercase Z
{	123	7B	Opening (Left) Brace
	124	7C	Vertical Line
}	125	7D	Closing (Right) Brace
~	126	7E	Tilde
	127	7F	Delete



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APPENDIX D

PROBLEM/SUGGESTION REPORT FORM

If you experience any problems while operating this product, or if you note any inaccuracies while reading the User's Manual, please copy this form, fill it out, then mail or fax it to ZiLOG (see "Return Information"). We also welcome your suggestions!

Customer Information

Name	_____	Country	_____
Company	_____	Telephone	_____
Address	_____	Fax Number	_____
City/State/ZIP	_____	E-Mail Address	_____

Product Information

Serial # or Board Fab #/Rev. #
Software Version
Manual Number
Host Computer Description/Type

Return Information

ZiLOG, Inc.
Support Products Director
910 E. Hamilton Ave., Suite 110, MS 4-3
Campbell, CA 95008
Fax Number: (408) 558-8300
Email: tools@zilog.com

Problem Description or Suggestion

Provide a complete description of the problem or your suggestion. If you are reporting a specific problem, include all steps leading up to the occurrence of the problem. Attach additional pages as necessary.

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